

Compal Confidential

EFL50/ EFT51 Schematics Document

Intel Dothan/ Celeron M/ Alviso GM(PM) / DDR-2 / ICH6-M

(Daughter Card: ATi M24P/ M26P)

2005 / 03 / 08 (DVT)

Rev:0.2

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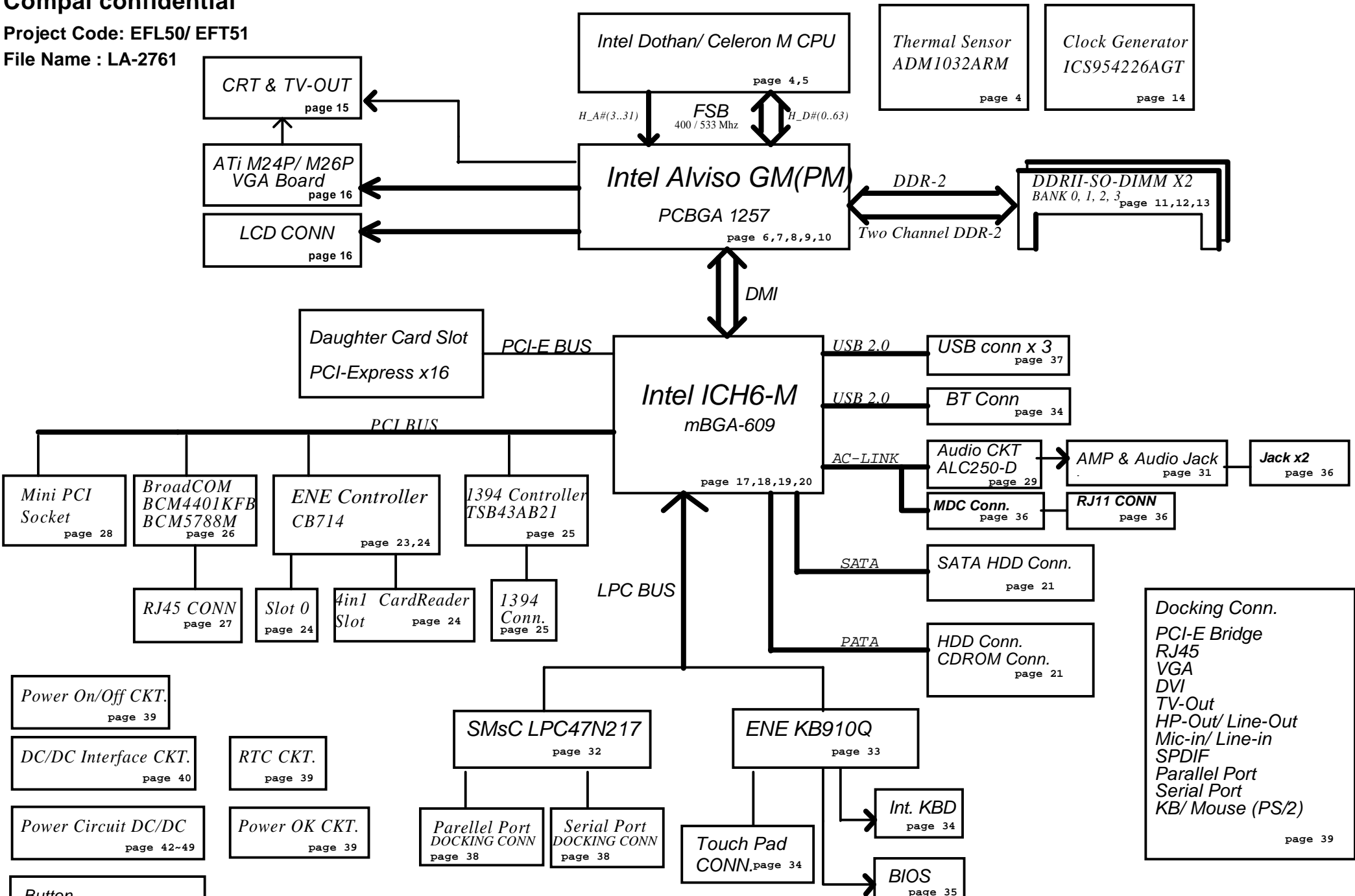
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Project Code: EFL50/ EFT51

File Name : LA-2761



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Power Plane	Description	S1	S3	S4/ S5
VIN	Adapter power supply (19V)	N/A	N/A	N/A
B+	AC or battery power rail for power circuit.	N/A	N/A	N/A
+CPU_CORE	Core voltage for CPU	ON	OFF	OFF
+1.05VS	1.05V switched power rail	ON	OFF	OFF
+DDRVTT	0.9V switched power rail for DDR terminator	ON	OFF	OFF
+1.5VALW	1.5V always on power rail	ON	ON	ON*
+1.5VS	1.5V switched power rail	ON	OFF	OFF
+1.8VS	1.8V switched power rail	ON	OFF	OFF
+DDRVC	1.8V power rail for DDR	ON	ON	OFF
+2.5VS	2.5V switched power rail	ON	OFF	OFF
+3VALW	3.3V always on power rail	ON	ON	ON*
+3V	3.3V power rail	ON	ON	OFF
+3VS	3.3V switched power rail	ON	OFF	OFF
+5VALW	5V always on power rail	ON	ON	ON*
+5VS	5V switched power rail	ON	OFF	OFF
+5VMOD	5V switched power rail for Module Bay	ON	OFF	OFF
+12VALW	12V always on power rail	ON	ON	ON*
+RTCVCC	RTC power	ON	ON	ON

External PCI Devices

Device	IDSEL#	REQ#/GNT#	Interrupts
CardBus	AD20	2	PIRQA/PIRQB
1394	AD16	0	PIRQE
SD	AD20	2	PIRQA/PIRQB
Mini-PCI	AD18	1	PIRQG/PIRQH
LAN	AD17	3	PIRQF

Device	Address	Device	Address
Smart Battery	0001 011X b	ADM1032	1001 110X b
EEPROM(24C16/02)	1010 000X b		
(24C04)	1011 000Xb		

Device	Address
Clock Generator (ICS 954226AGT)	1101 001Xb
DDRII DIMM0	1001 000Xb
DDRII DIMM2	1001 010Xb

STATE \ SIGNAL	SLP_S1#	SLP_S3#	SLP_S4#	SLP_S5#	+VAL#	+V	+VS	Clock
Full ON	HIGH	HIGH	HIGH	HIGH	ON	ON	ON	ON
S1 (Power On Suspend)	LOW	HIGH	HIGH	HIGH	ON	ON	ON	LOW
S3 (Suspend to RAM)	LOW	LOW	HIGH	HIGH	ON	ON	OFF	OFF
S4 (Suspend to Disk)	LOW	LOW	LOW	HIGH	ON	OFF	OFF	OFF
S5 (Soft OFF)	LOW	LOW	LOW	LOW	ON	OFF	OFF	OFF

Vcc	3.3V +/- 5%			
Ra / Rc	100K +/- 5%			
Board ID	Rb / Rd	VAD_BID min	VAD_BID typ	VAD_BID max
0	0	0 V	0 V	0 V
1	8.2K +/- 5%	0.216 V	0.250 V	0.289 V
2	18K +/- 5%	0.436 V	0.503 V	0.538 V
3	33K +/- 5%	0.712 V	0.819 V	0.875 V
4	56K +/- 5%	1.036 V	1.185 V	1.264 V
5	100K +/- 5%	1.453 V	1.650 V	1.759 V
6	200K +/- 5%	1.935 V	2.200 V	2.341 V
7	NC	2.500 V	3.300 V	3.300 V

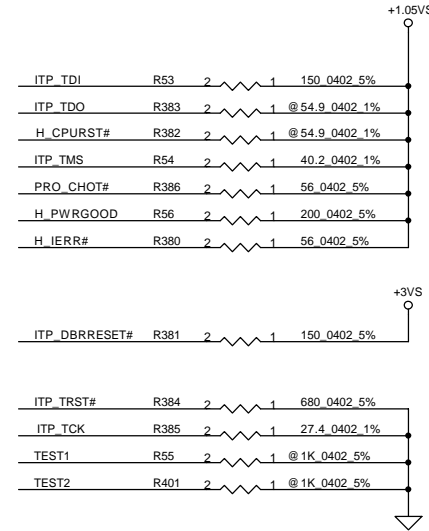
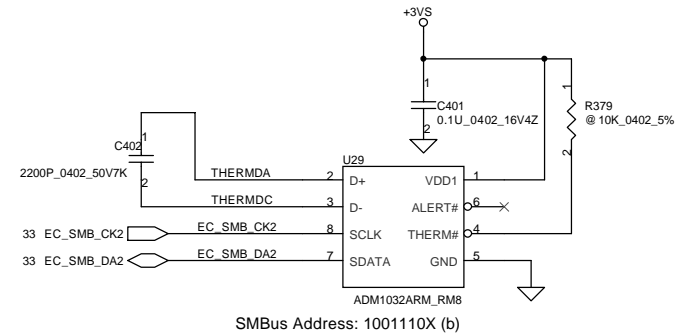
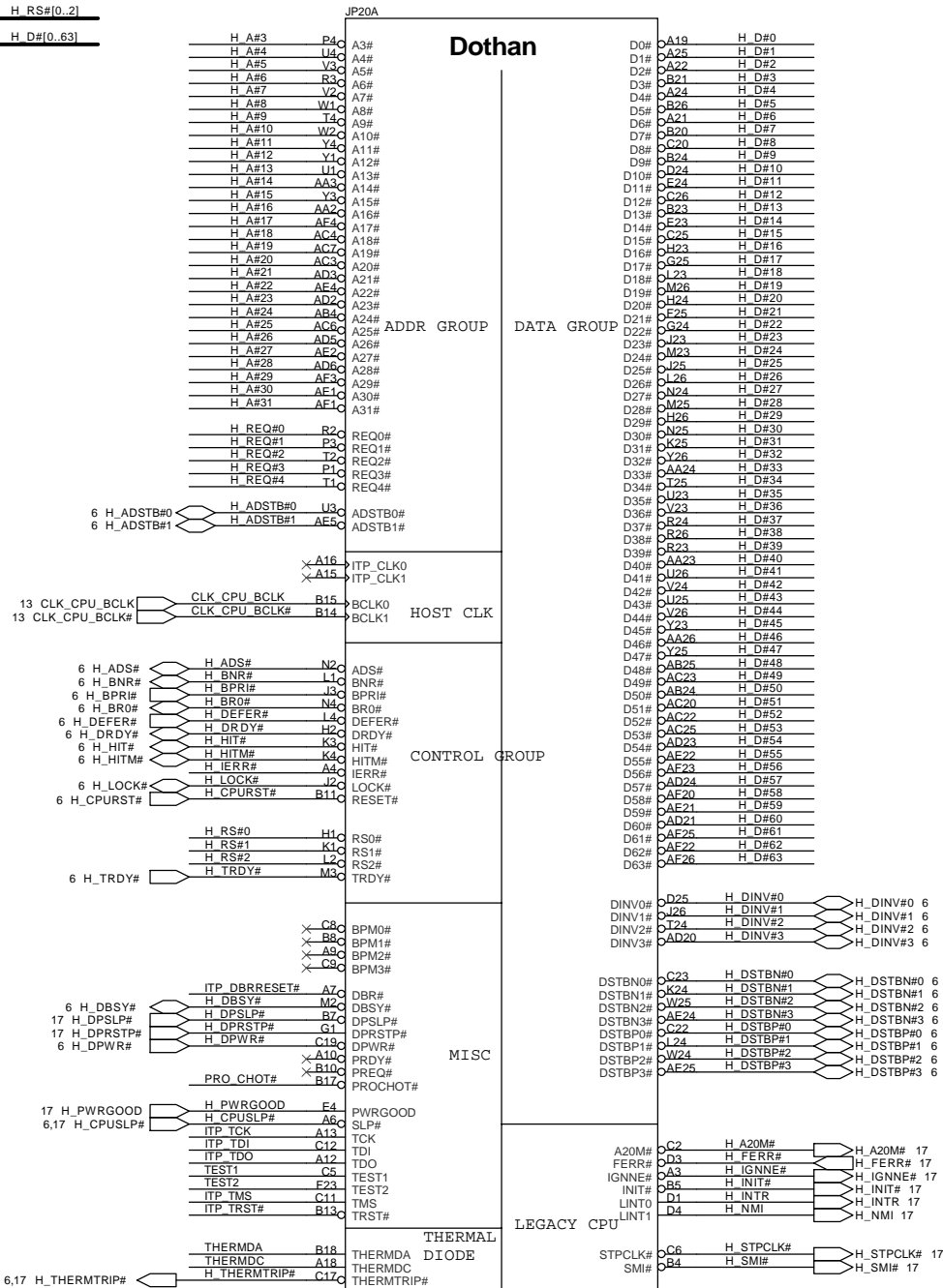
Board ID	PCB Revision
0	0.1
1	
2	
3	
4	
5	
6	
7	

SKU ID	SKU
0	
1	
2	
3	
4	
5	
6	
7	

BTO Item	BOM Structure
UMA	GM@
Discrete	PM@
LAN 10/100	4401@
LAN GIGA	5788@
1 Spindle	1S@
2 Spindle	2S@
2 Spindle with SATA	2SS@
2 Spindle with PATA	2SP@
1 Spindle with SATA	1SS@
1 Spindle with PATA	1SP@
With Docking	WD@
Without Docking	ND@
With 1394	1394@
With 1394 4pin	1394<4>@
With 1394 6pin	1394<6>@

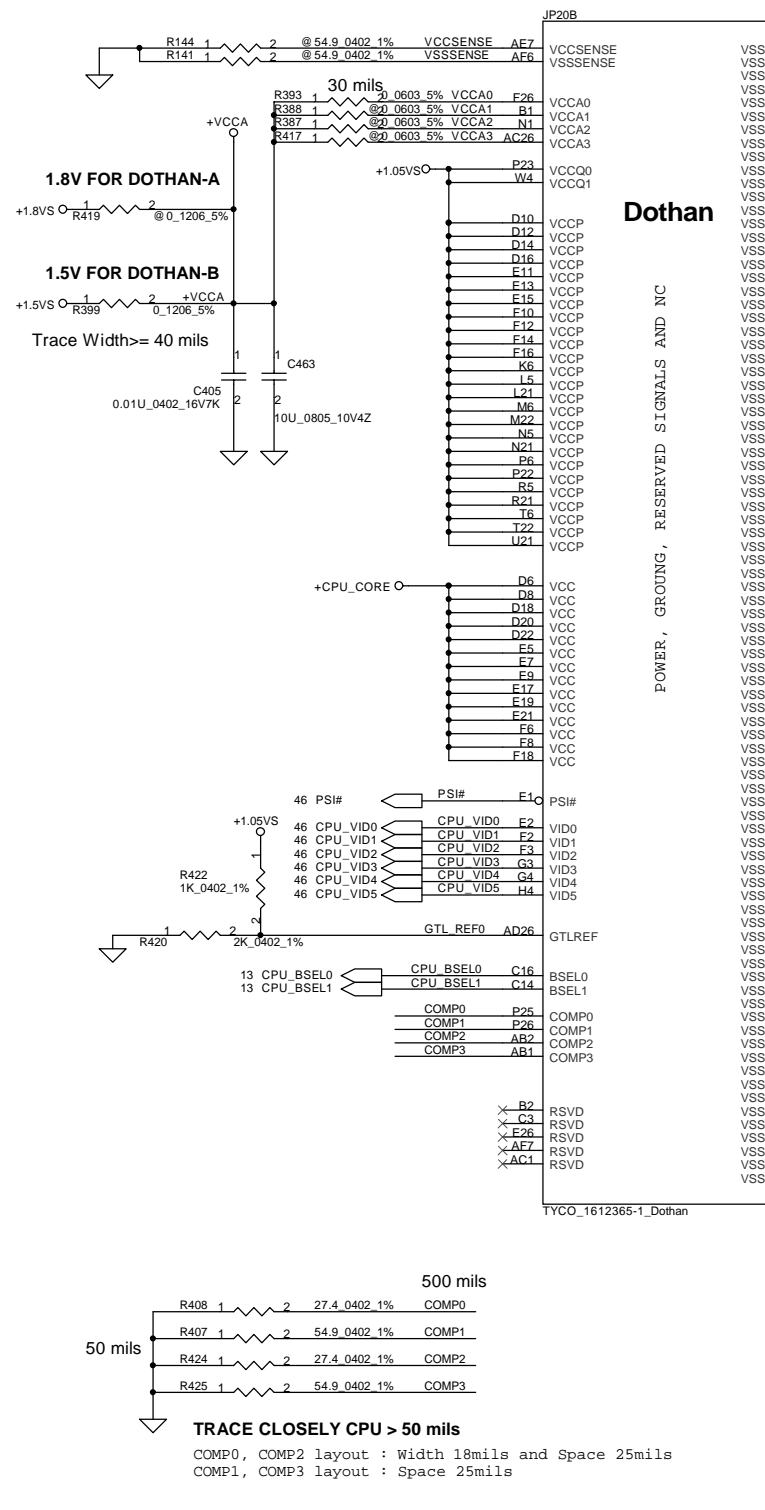
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6 H_A#[3..31] H_A#[3..31]
6 H_REQ#[0..4] H_REQ#[0..4]
6 H_RS#[0..2] H_RS#[0..2]
6 H_D#[0..63] H_D#[0..63]



THERMDA & THERMDC Trace / Space = 10 / 10 mil

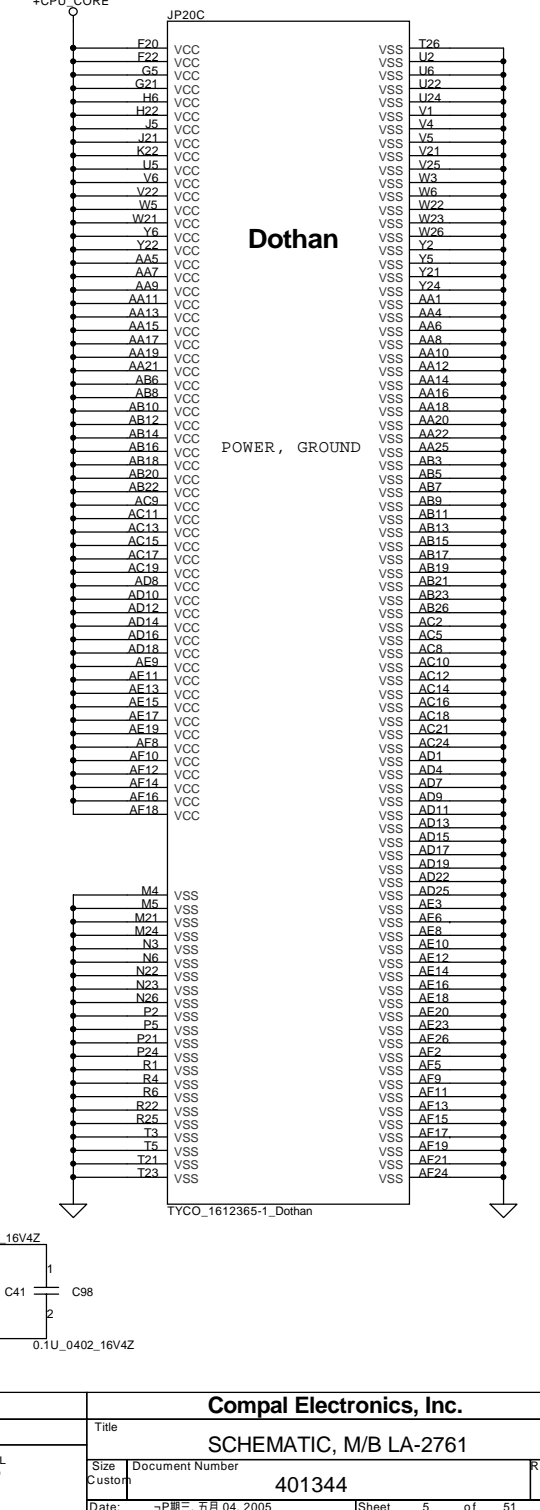
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Dothan

POWER, GROUND, RESERVED SIGNALS AND NC

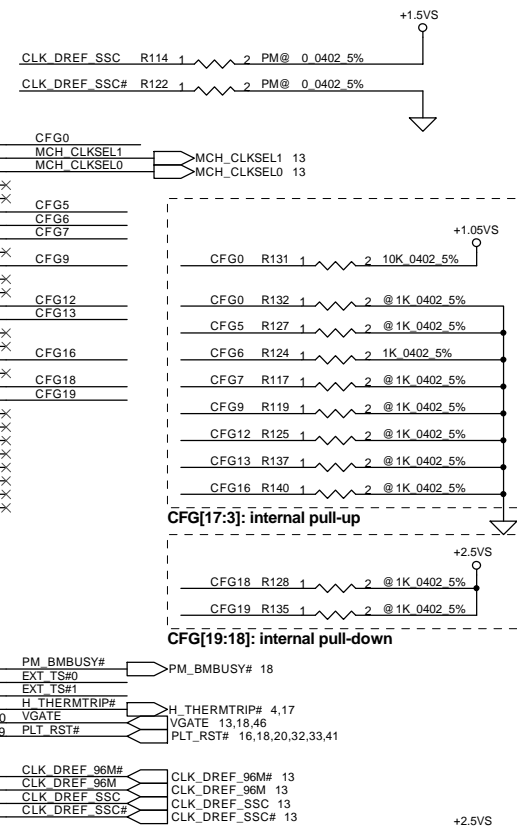
Vcc-core Decoupling	C,uF	ESR, mohm	ESL,nH
SPCAP, Polymer	3X330uF	9m ohm/3	3.5nH/4
MLCC 0805 X5R	35X10uF	5m ohm/35	0.6nH/35



Dothan

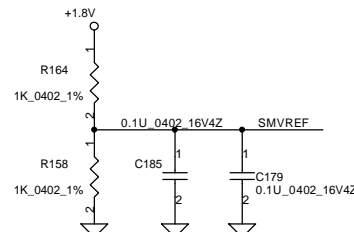
POWER, GROUND

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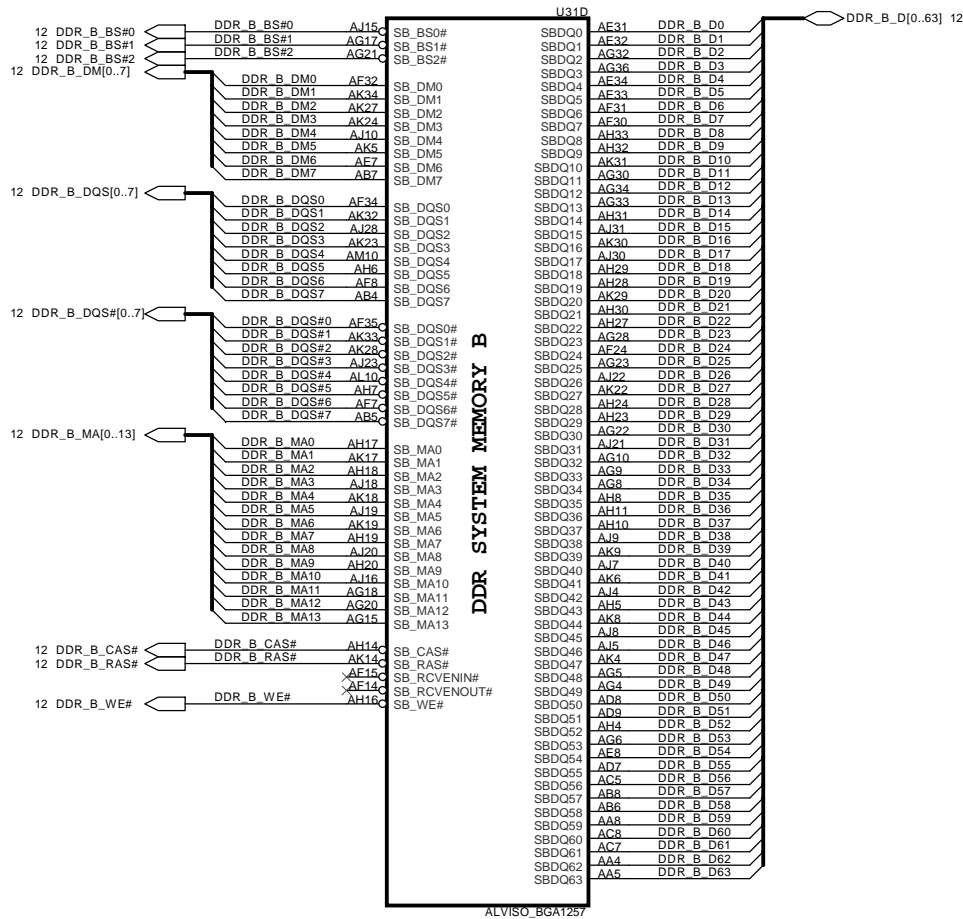
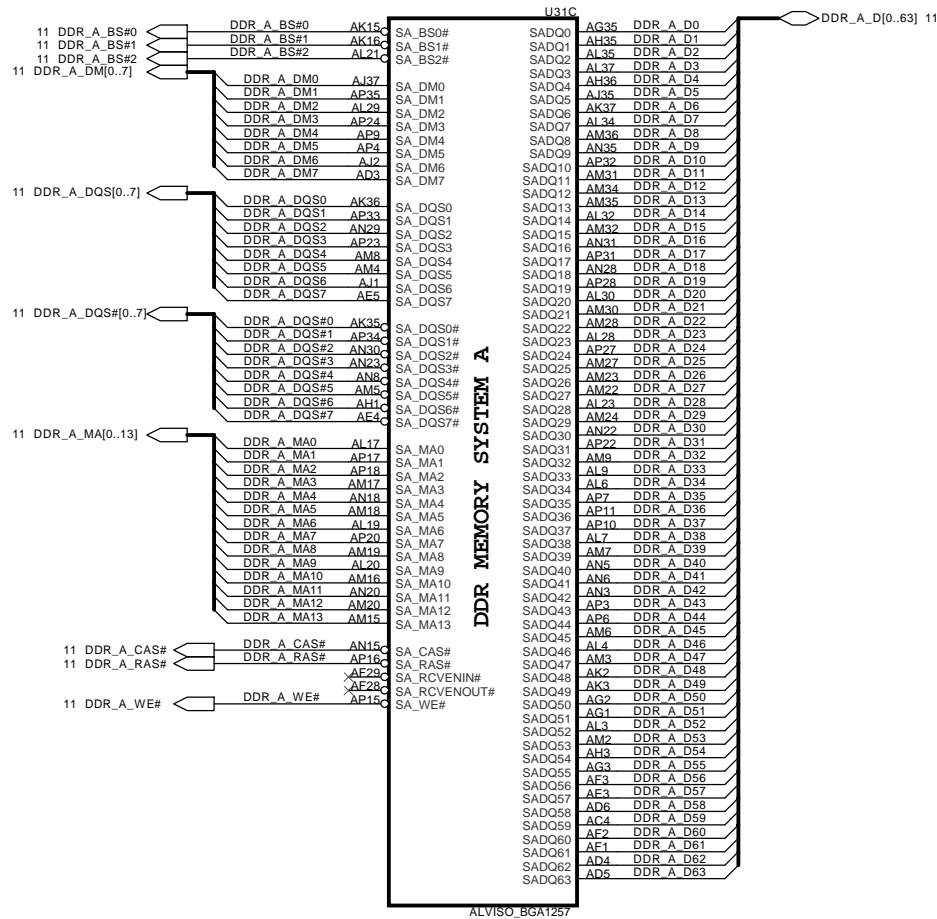


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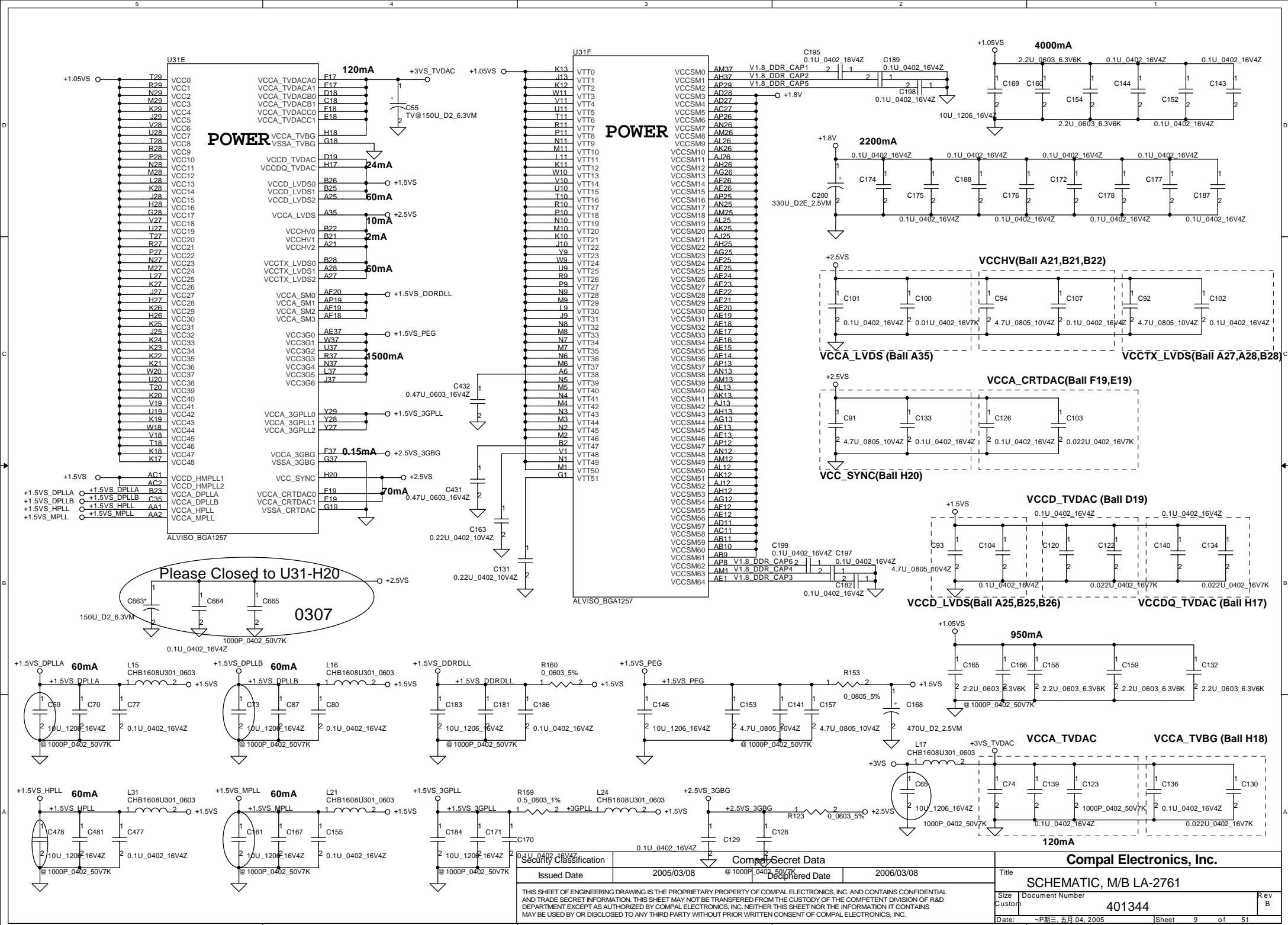
AL VISO BGA1257

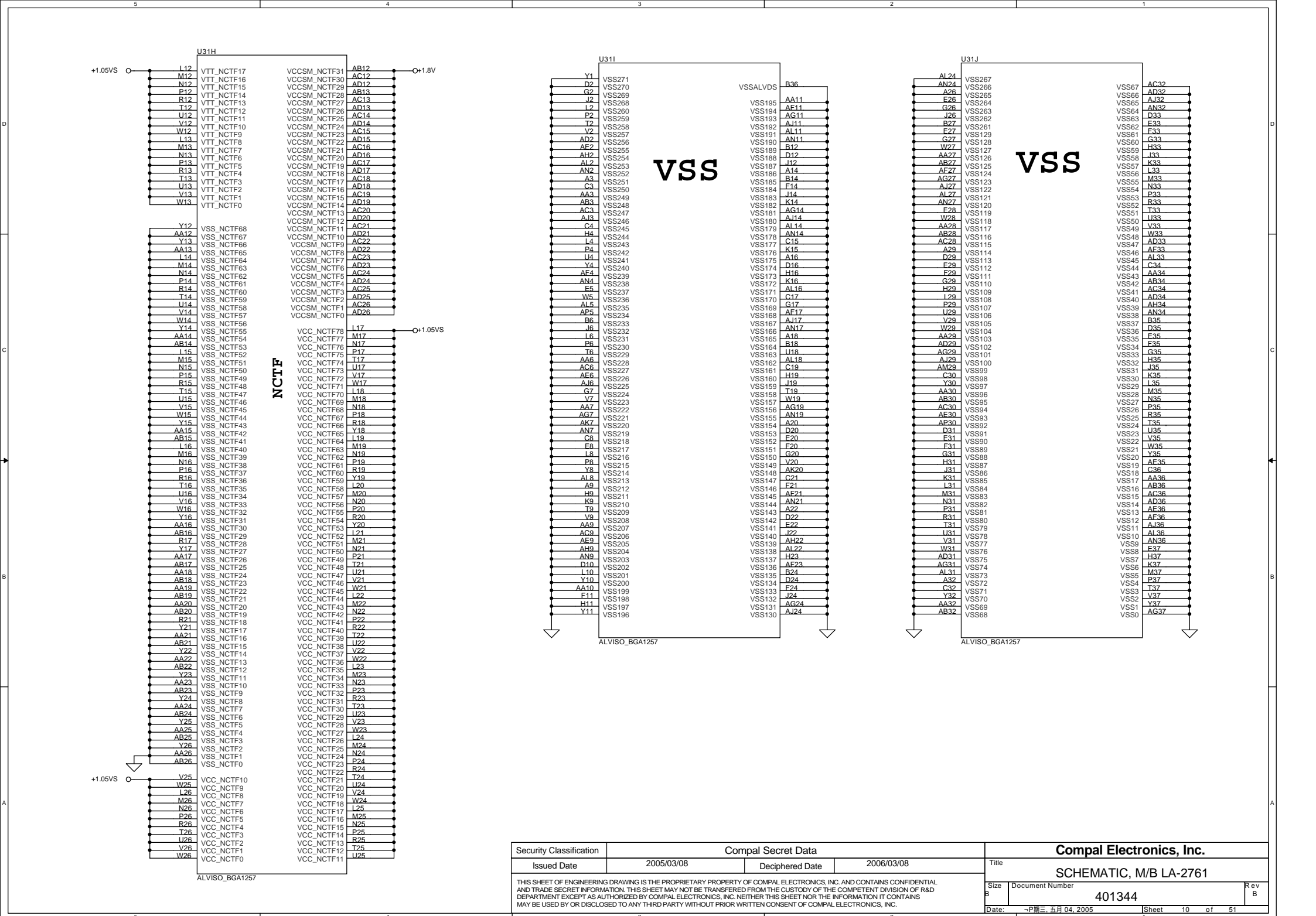


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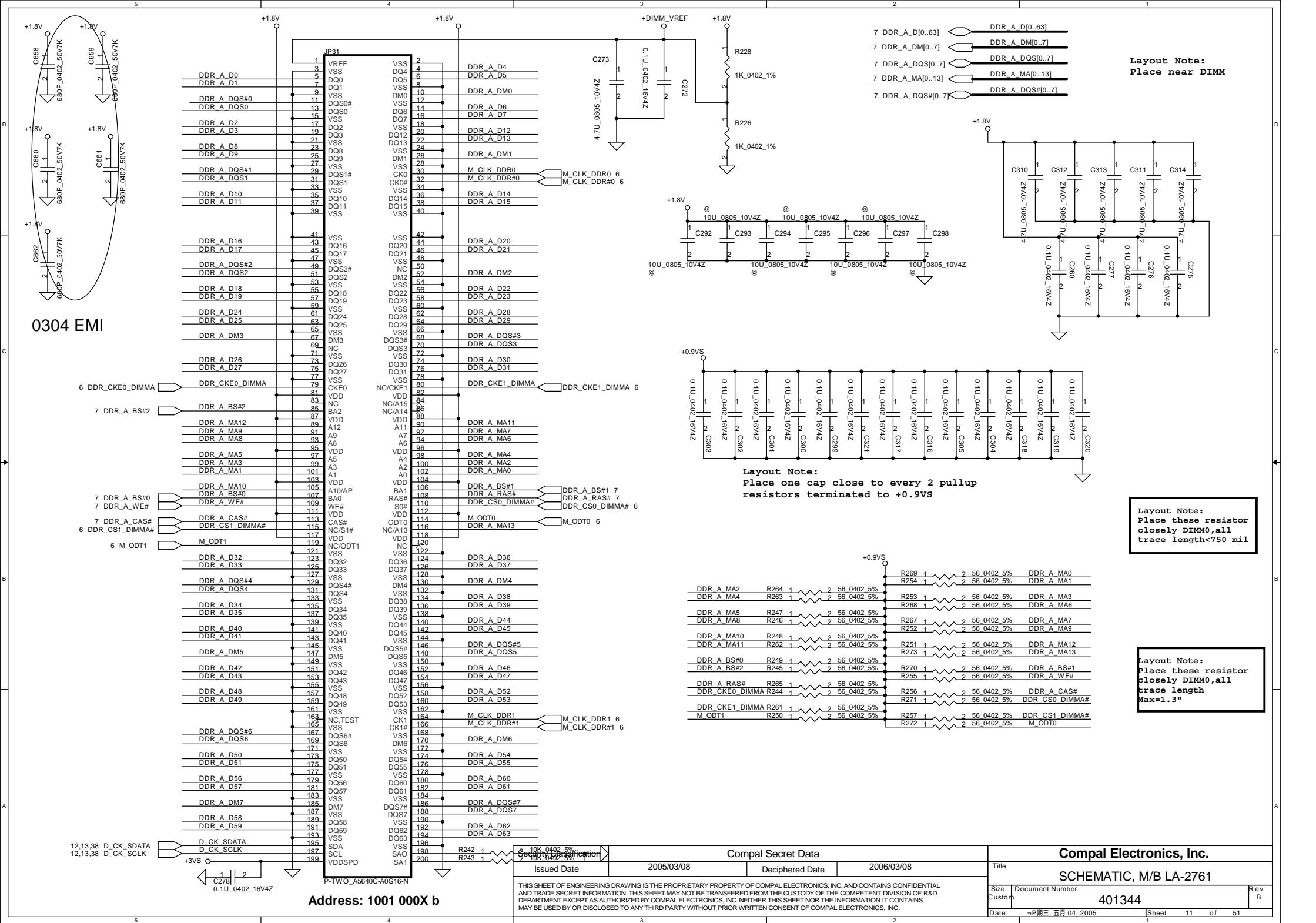


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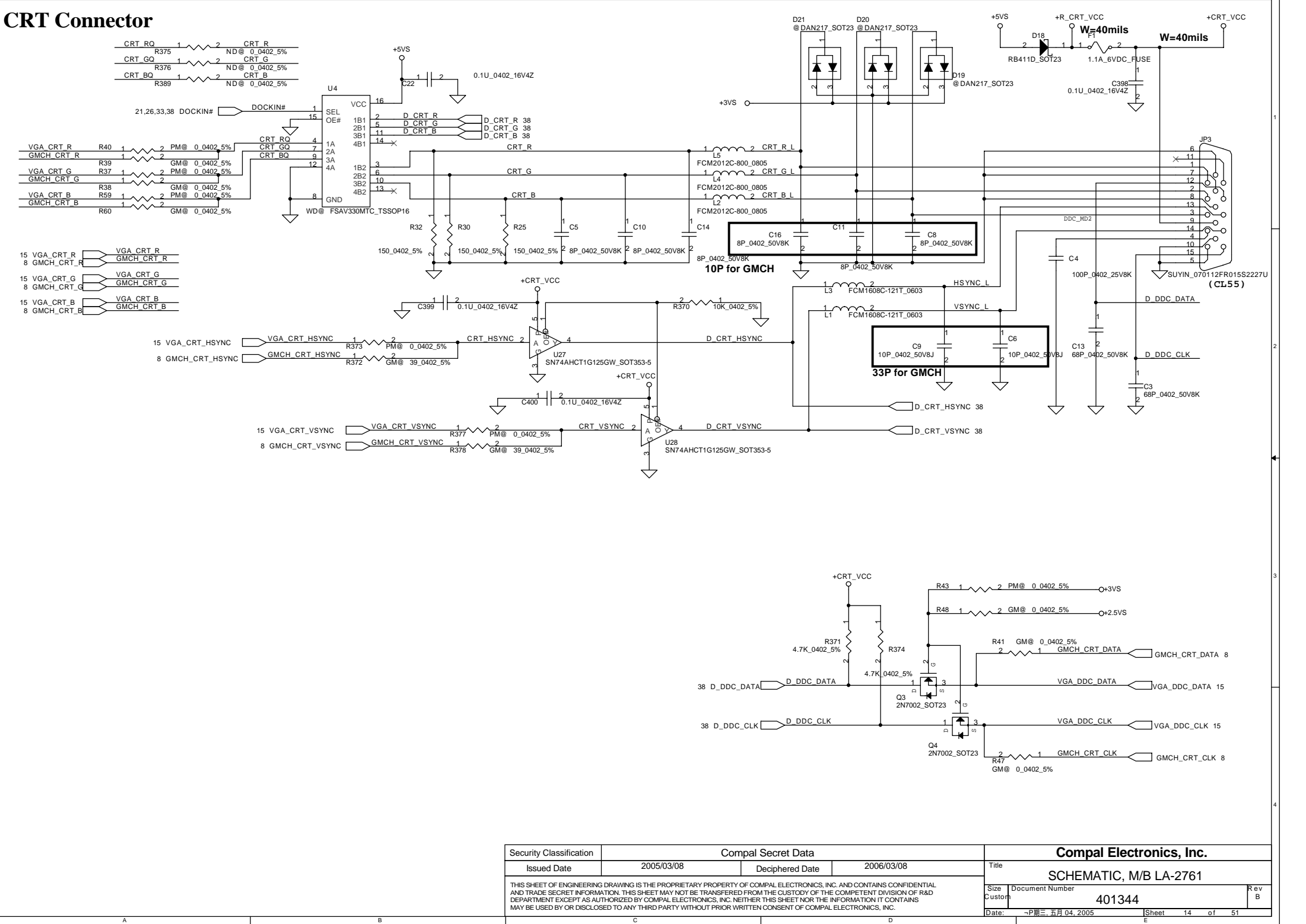




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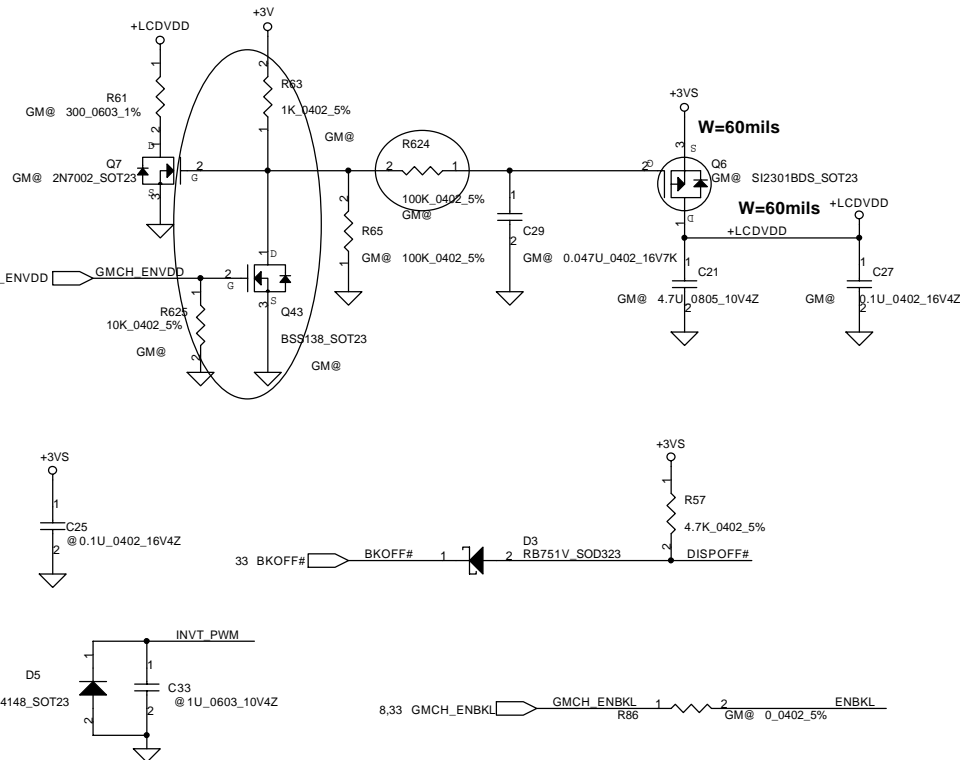


CRT Connector

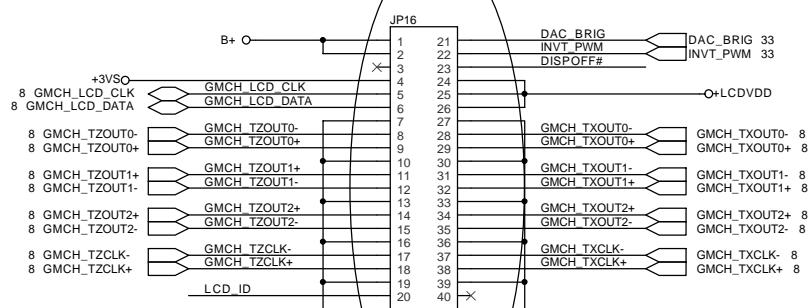


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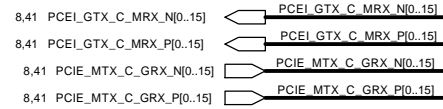
LCD POWER CIRCUIT



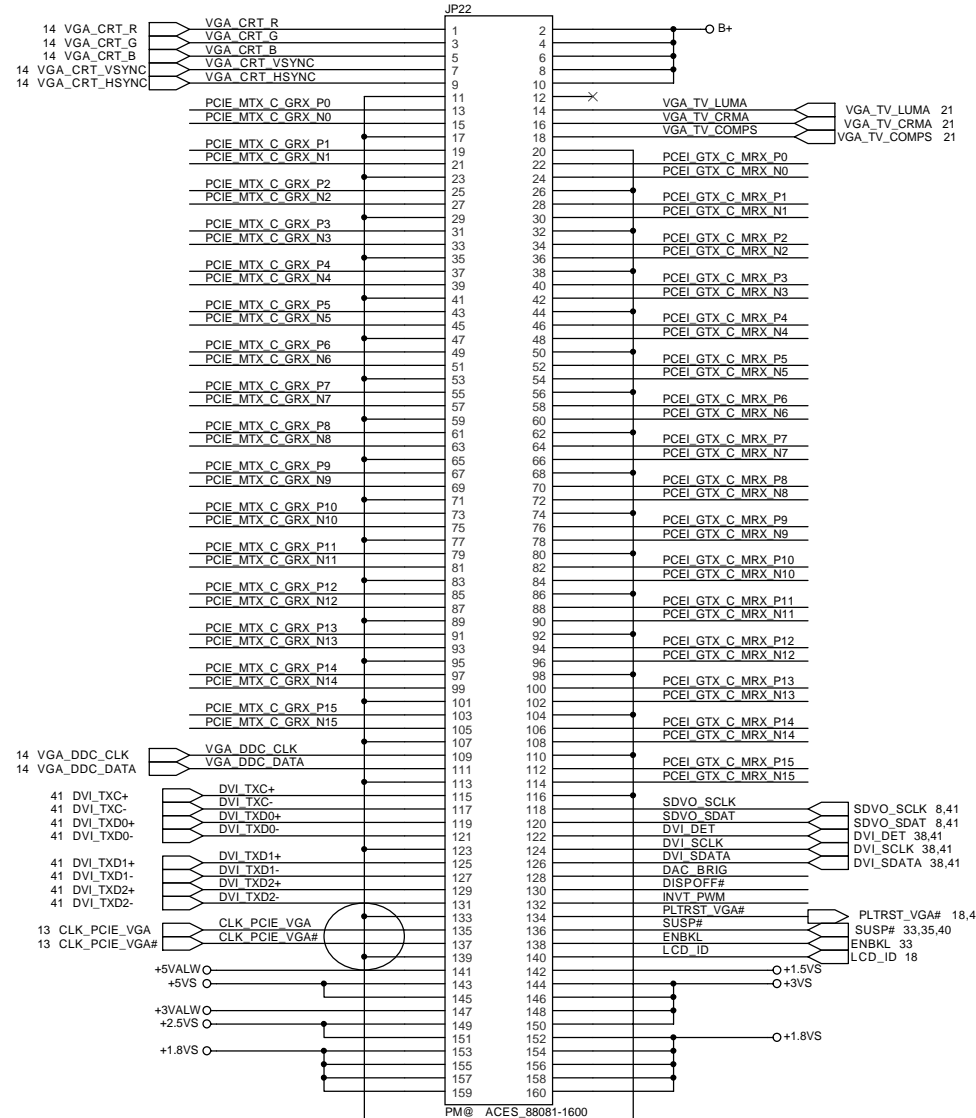
LCD/PANEL BD. Conn.



Reverse for Layout

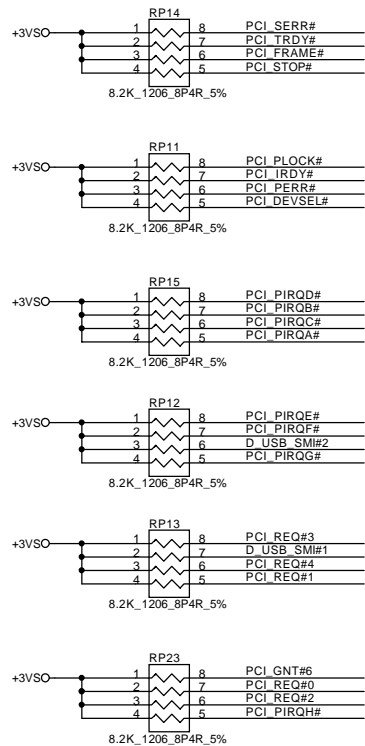


VGA BOARD Conn.



2005/01/24

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22,24,25,27,28 PCI_AD[0..31]

22,24,25,27,28 PCI_FRAME#
22 PCI_PIRQA#
22 PCI_PIRQB#

PCI AD0 E2
PCI AD1 E5
PCI AD2 C2
PCI AD3 F5
PCI AD4 E3
PCI AD5 E9
PCI AD6 F2
PCI AD7 D6
PCI AD8 E6
PCI AD9 D3
PCI AD10 A2
PCI AD11 D2
PCI AD12 D5
PCI AD13 H3
PCI AD14 B4
PCI AD15 J5
PCI AD16 K2
PCI AD17 K5
PCI AD18 D4
PCI AD19 L6
PCI AD20 G3
PCI AD21 H4
PCI AD22 H2
PCI AD23 H5
PCI AD24 B3
PCI AD25 M6
PCI AD26 B2
PCI AD27 K6
PCI AD28 K3
PCI AD29 A5
PCI AD30 L1
PCI AD31 K4

✕AC5
✕AD5
✕AF4
✕AG4
✕AG9
✕AF8
✕AG8
✕U3

ICH6_BGA609

U13B

PCI

REQ[0]# L5 PCI_REQ#0 PCI_REQ#0 24
GNT[0]# C1 PCI_GNT#0 PCI_GNT#0 24
REQ[1]# B6 PCI_REQ#1 PCI_REQ#1 27,28
GNT[1]# M5 PCI_GNT#1 PCI_GNT#1 27,28
REQ[2]# F1 PCI_REQ#2 PCI_REQ#2 22
GNT[2]# B8 PCI_GNT#2 PCI_GNT#2 22
REQ[3]# C8 PCI_REQ#3 PCI_REQ#3 25
GNT[3]# F7 PCI_GNT#3 PCI_GNT#3 25
REQ[4]#/GPI[40] E7
GNT[4]#/GPO[48] E8 D_USB_SMI#1 38
REQ[5]#/GPI[1] F6 PCI_GNT#5 D_USB_SMI#1 38
GNT[5]#/GPO[17] B7 D_USB_SMI#2 38
REQ[6]#/GPI[0] D8 PCI_GNT#6
GNT[6]#/GPO[16] J6 PCI_CBE#0 PCI_C/BE#0 22,24,25,27,28
C/BE[0]# H6 PCI_CBE#1 PCI_C/BE#1 22,24,25,27,28
C/BE[1]# G4 PCI_CBE#2 PCI_C/BE#2 22,24,25,27,28
C/BE[2]# G2 PCI_CBE#3 PCI_C/BE#3 22,24,25,27,28
C/BE[3]# A3 PCI_IRDY# PCI_IRDY# 22,24,25,27,28
IRDY# E1 PCI_PAR PCI_PAR 22,24,25,27,28
PAR B2 PCI_RST# PCI_RST# 22,24,25,27,28,32,33
PCIRST# C3 PCI_DEVSEL# PCI_DEVSEL# 22,24,25,27,28
DEVSEL# E3 PCI_PERR# PCI_PERR# 22,24,25,27,28
PERR# C5 PCI_PLOCK#
PLOCK# G5 PCI_SERR# PCI_SERR# 22,24,25,27,28
SERR# J1 PCI_STOP# PCI_STOP# 22,24,25,27,28
STOP# J2 PCI_TRDY# PCI_TRDY# 22,24,25,27,28
TRDY#
PLTRST# R5 PLT_RST# PLT_RST# 6,18,20,32,33,41
PCICLK G6 CLK_ICH_PCI CLK_PCI_ICH 13
PME# P6

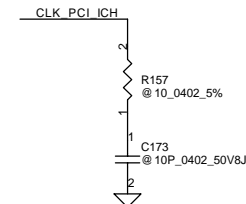
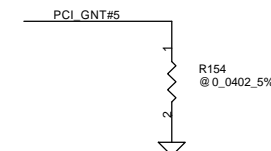
Interrupt I/F

PIRQ[A]# PIRO[E]#/GPI[2] D9 PCI_PIRQE# PCI_PIRQE# 24
PIRQ[B]# PIRO[F]#/GPI[3] C7 PCI_PIRQF# PCI_PIRQF# 25
PIRQ[C]# PIRO[G]#/GPI[4] C6 PCI_PIRQG# PCI_PIRQG# 27,28
PIRQ[D]# PIRO[H]#/GPI[5] M3 PCI_PIRQH# PCI_PIRQH# 27,28

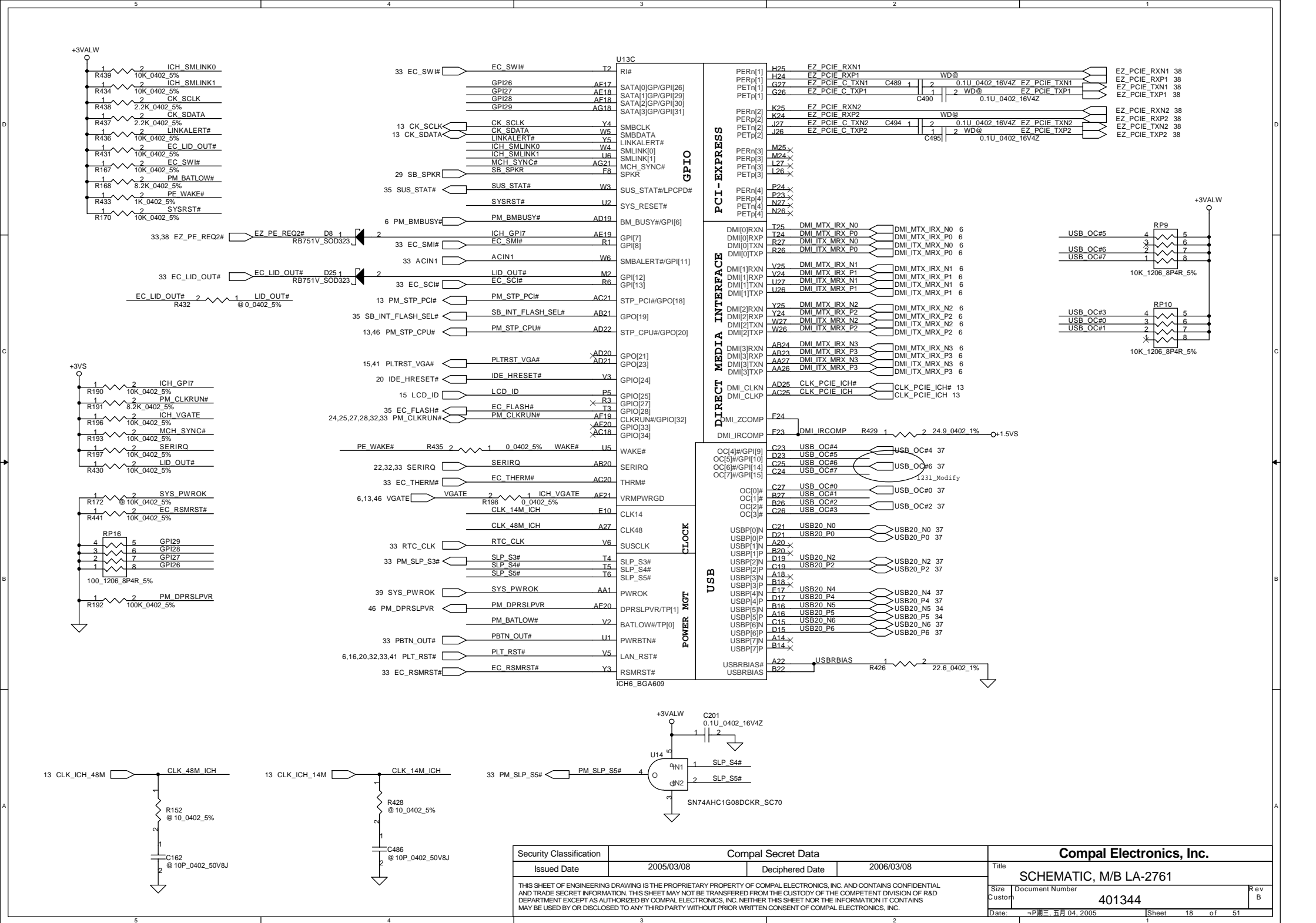
RESERVED

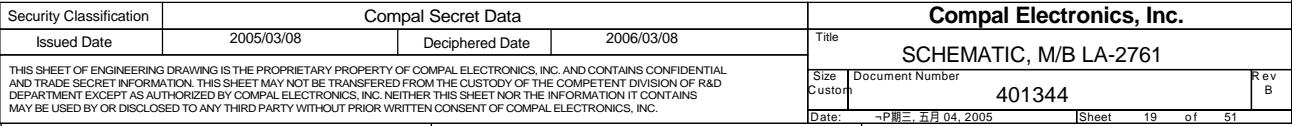
SATA[1]RXN/RSVD[1]
SATA[1]RXP/RSVD[2]
SATA[1]TXN/RSVD[3]
SATA[1]TXP/RSVD[4]
SATA[3]RXN/RSVD[5]
SATA[3]RXP/RSVD[6]
SATA[3]TXN/RSVD[7]
SATA[3]TXP/RSVD[8]
TP[3]RSVD[9]

Internal Pull-up.
Sample high destination is LPC.

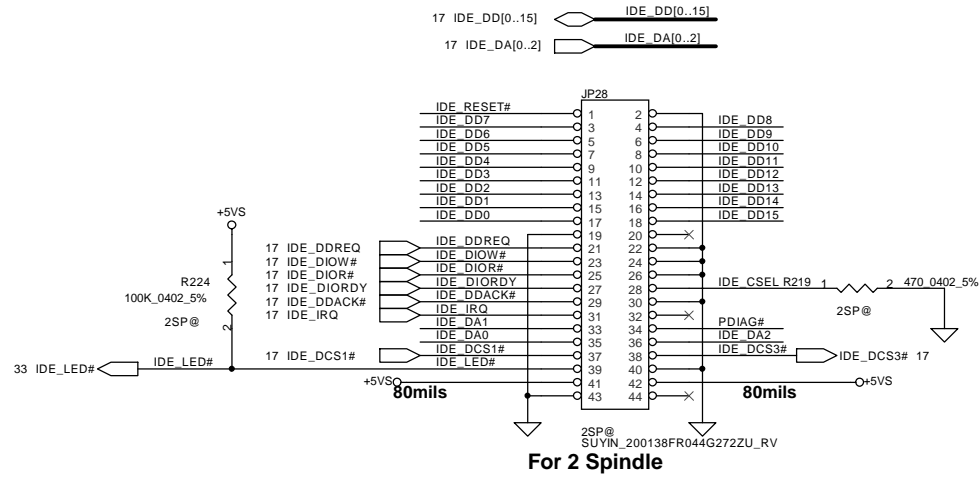


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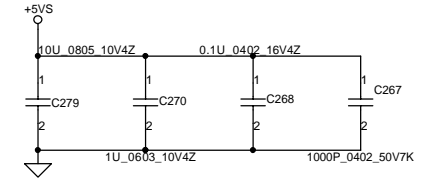
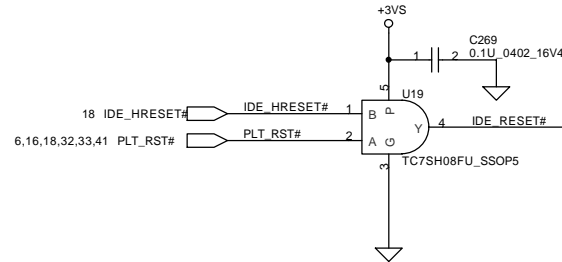
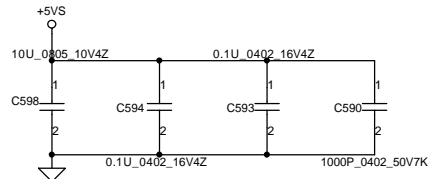
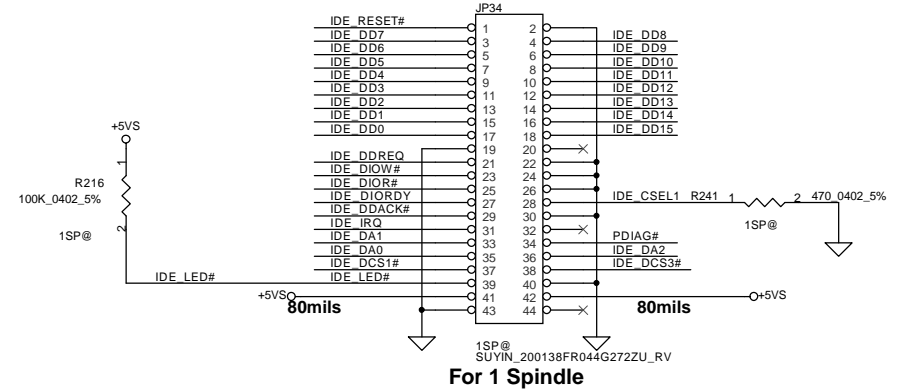




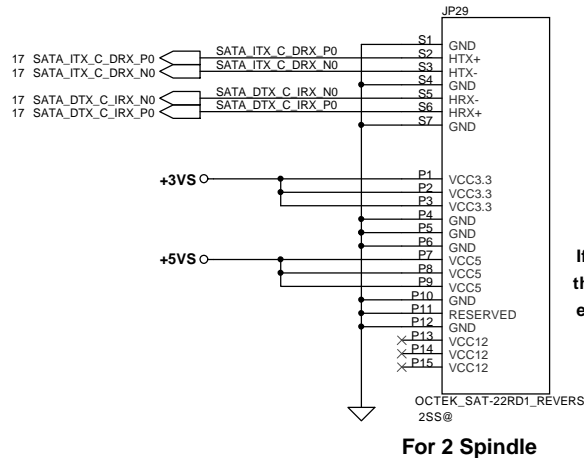
HDD CONN



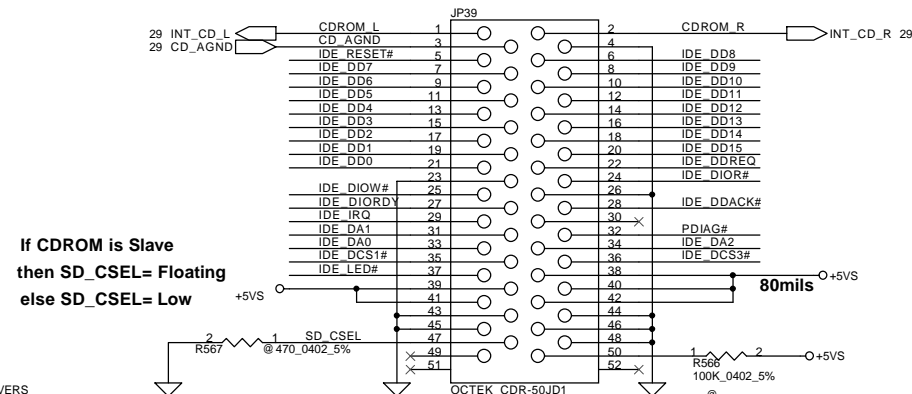
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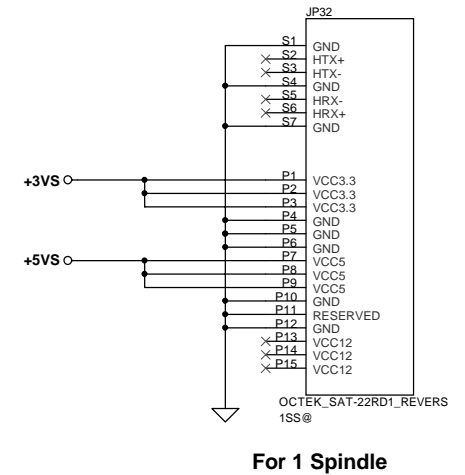
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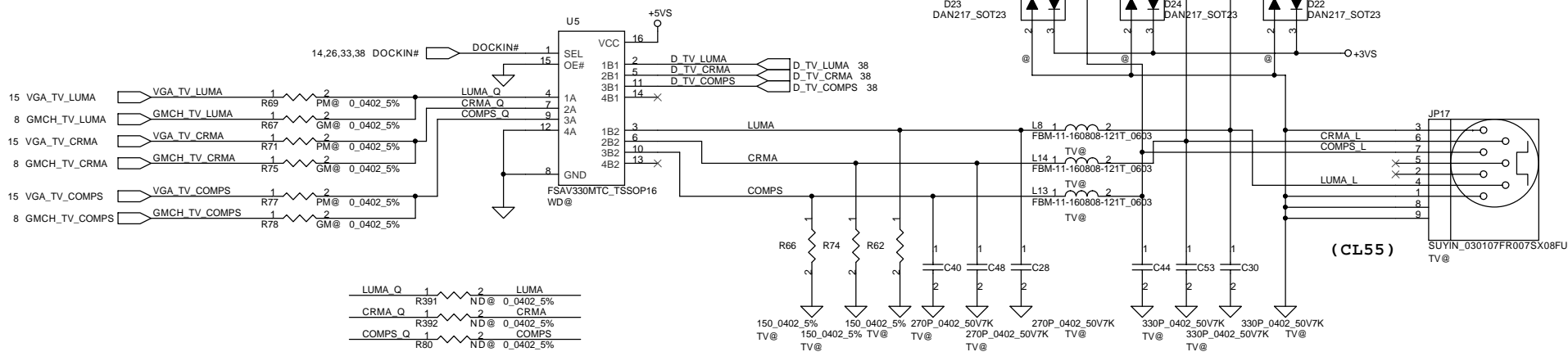
CDROM CONN



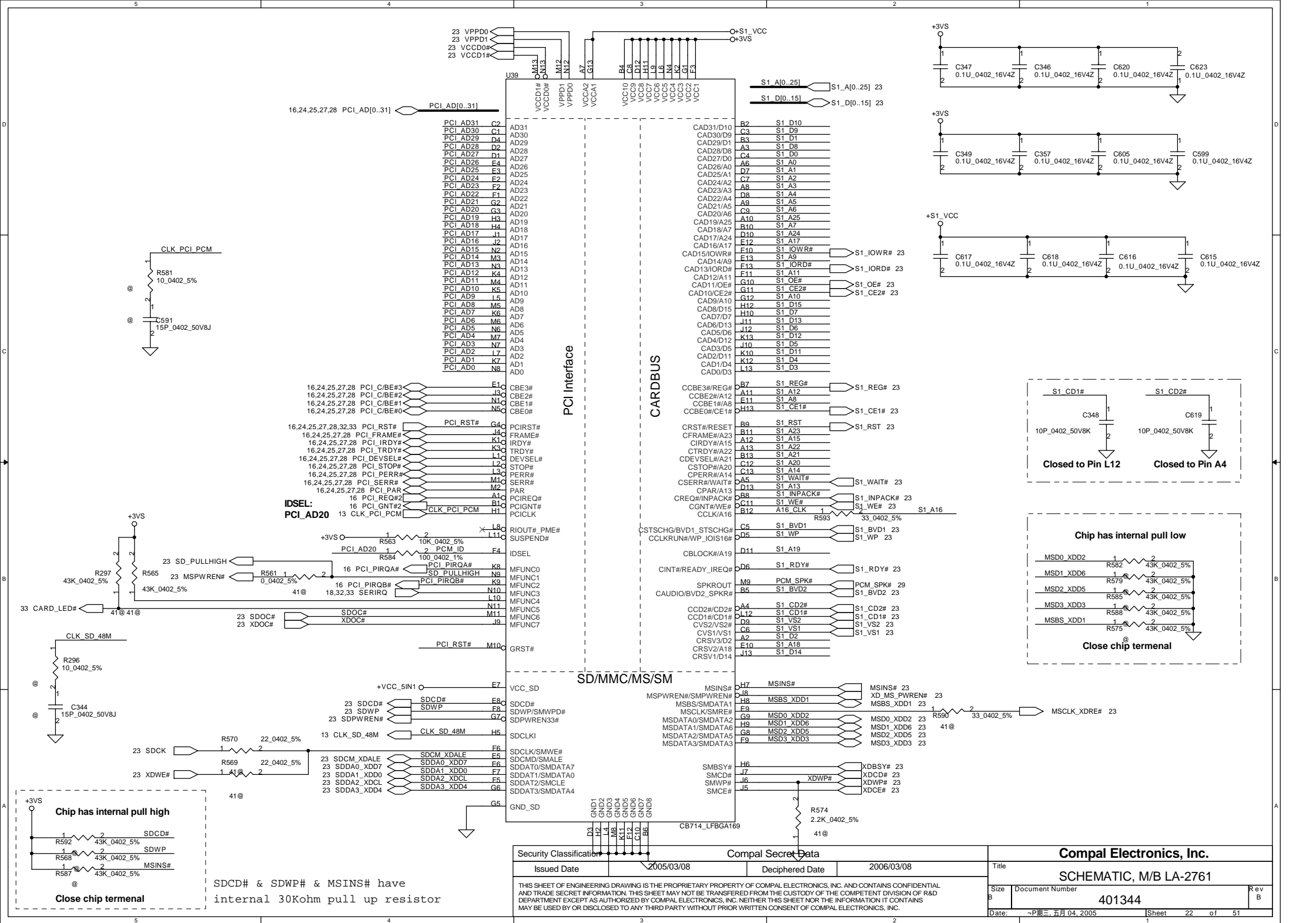
SATA HDD CONN



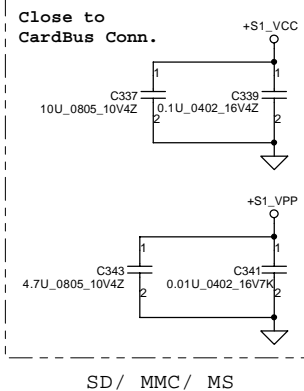
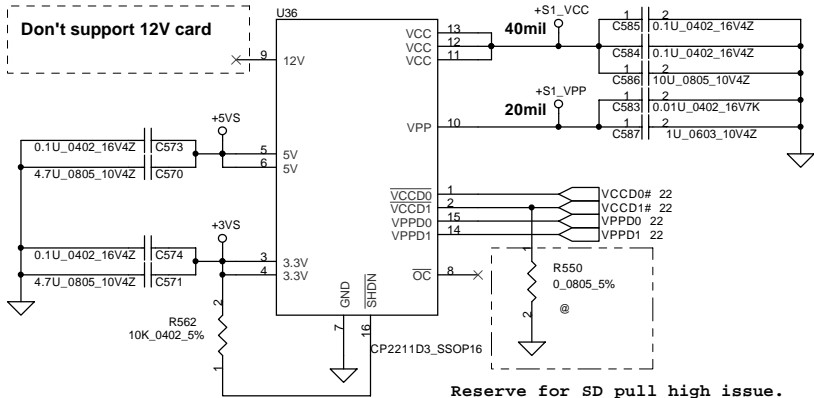
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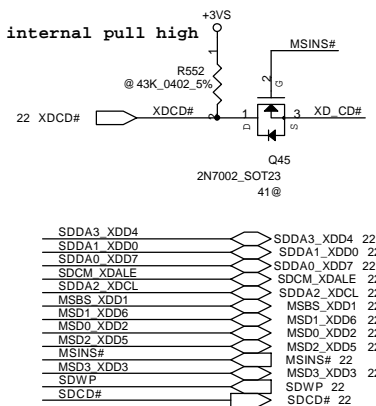
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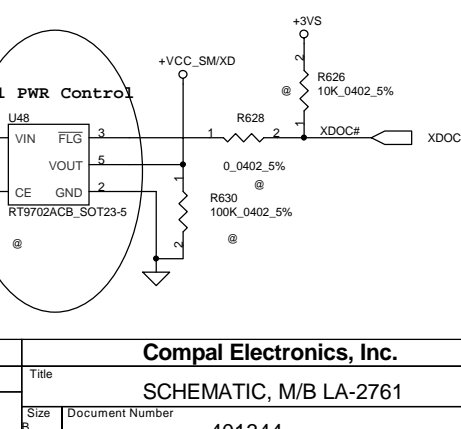
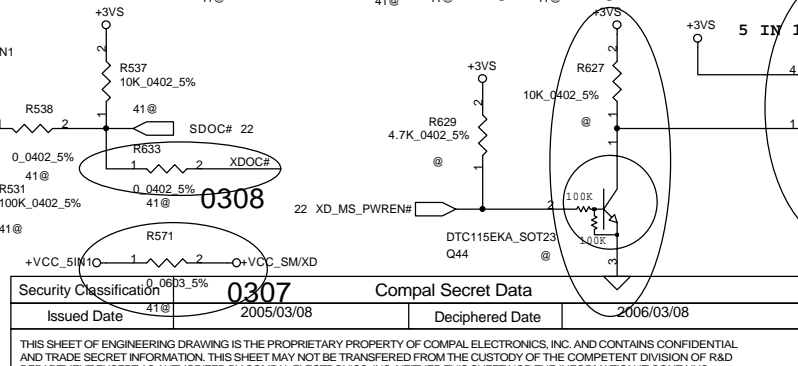
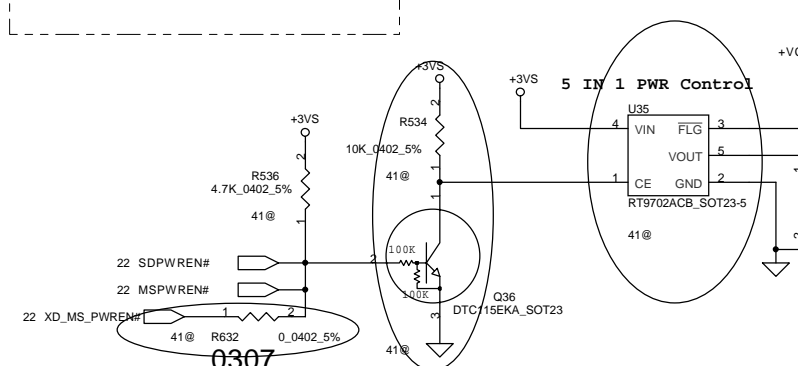
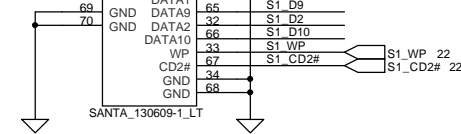
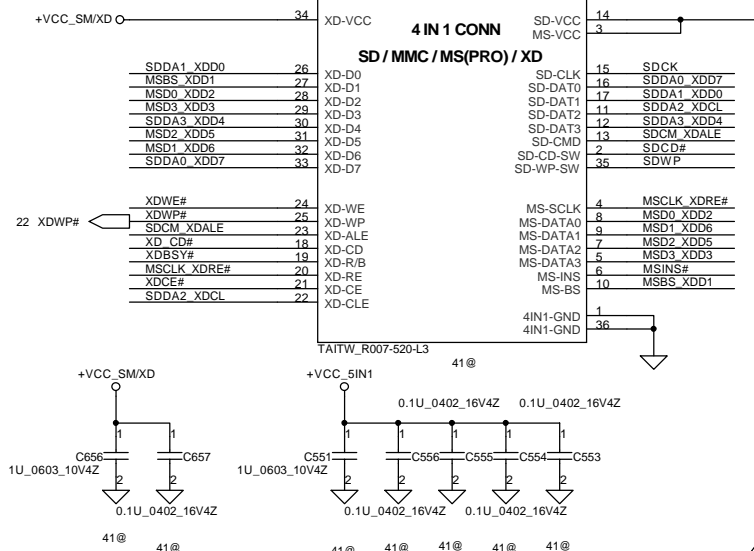
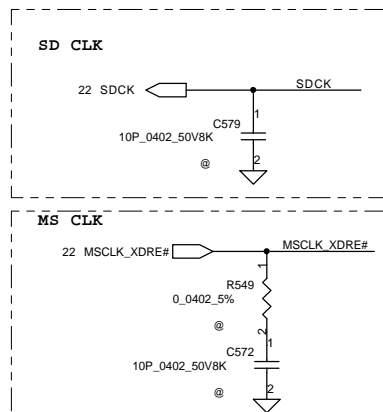
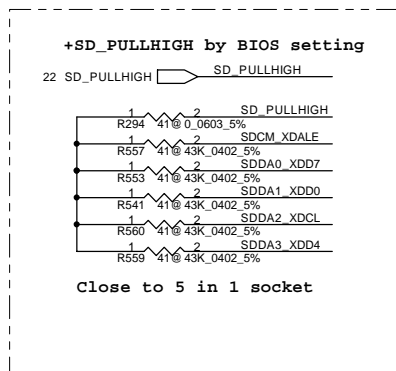
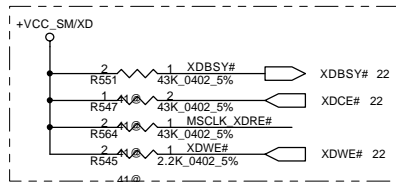
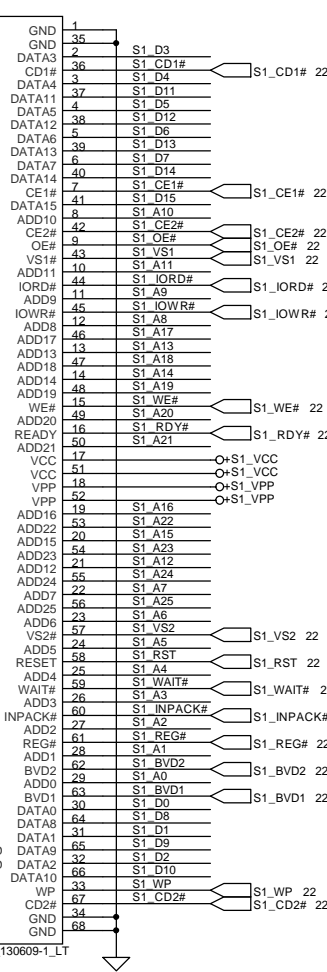
PCMCIA Power Controller



Chip has internal pull high

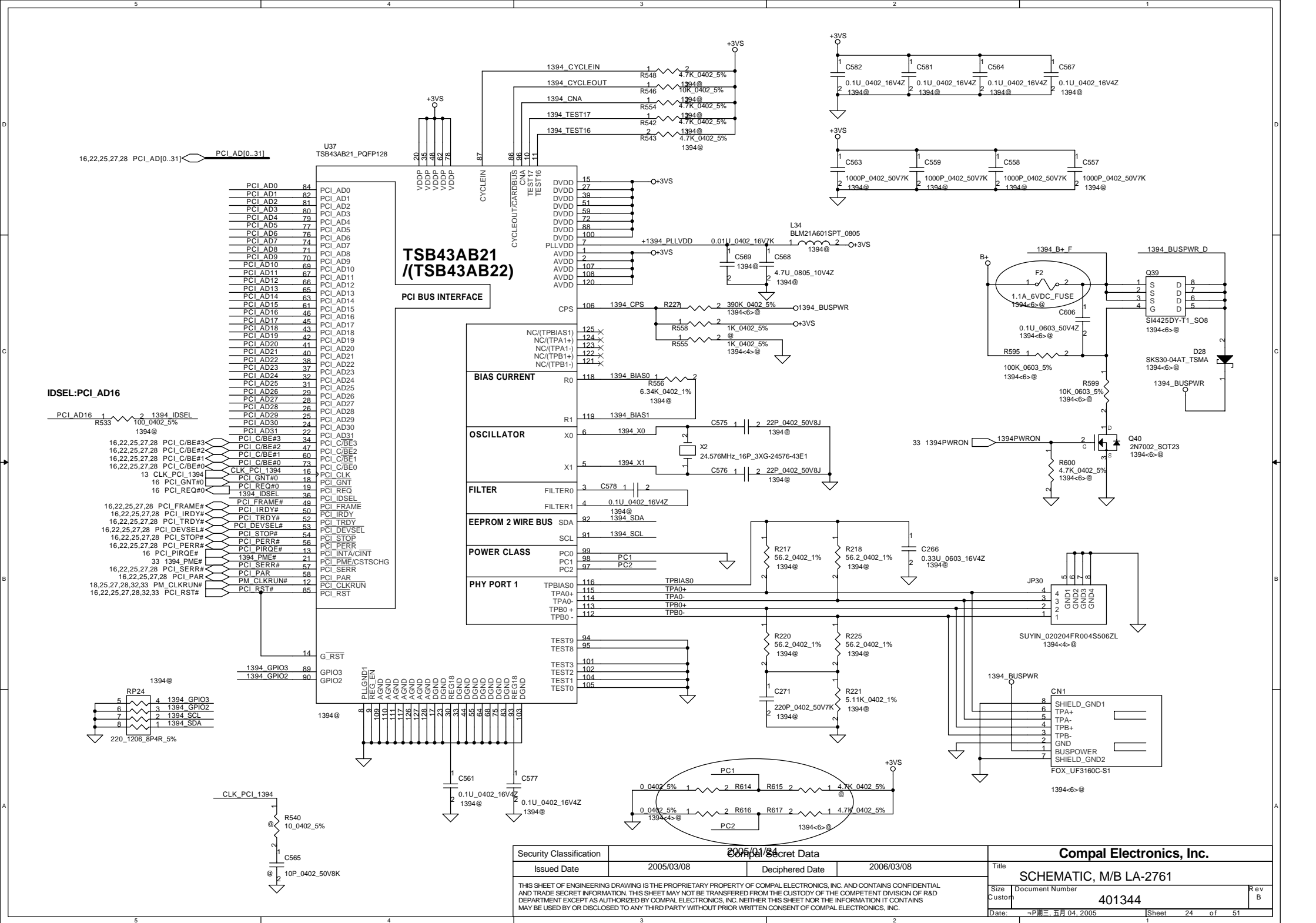


CardBus Socket



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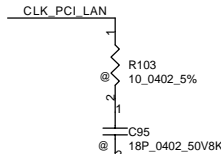
EN_WOL# = Low,
System can wake on LAN
(keep Low when Power On)

VGS(th) = -0.45V
IDmax = 2.3A

+3VS for BCM5788
+3V_LAN for BCM4401

16,22,24,27,28 PCI_AD[0..31]

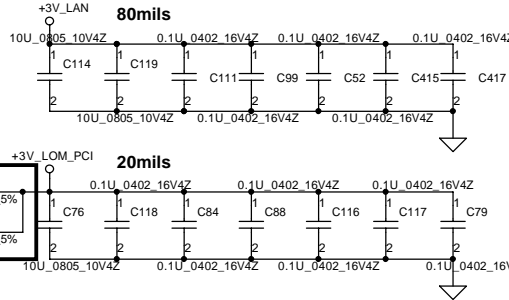
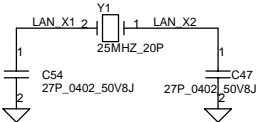
U8A
PCI_AD31 B8 AD31
PCI_AD30 A8 AD30
PCI_AD29 C7 AD29
PCI_AD28 C6 AD28
PCI_AD27 B6 AD27
PCI_AD26 B5 AD26
PCI_AD25 A5 AD25
PCI_AD24 B4 AD24
PCI_AD23 B2 AD23
PCI_AD22 B1 AD22
PCI_AD21 C1 AD21
PCI_AD20 D3 AD20
PCI_AD19 D2 AD19
PCI_AD18 D1 AD18
PCI_AD17 E3 AD17
PCI_AD16 K1 AD16
PCI_AD15 K2 AD15
PCI_AD14 L1 AD14
PCI_AD13 M3 AD13
PCI_AD12 M2 AD12
PCI_AD11 M1 AD11
PCI_AD10 N2 AD10
PCI_AD9 N3 AD9
PCI_AD8 P3 AD8
PCI_AD7 N4 AD7
PCI_AD6 P4 AD6
PCI_AD5 M5 AD5
PCI_AD4 M6 AD4
PCI_AD3 P5 AD3
PCI_AD2 P6 AD2
PCI_AD1 M7 AD1
PCI_AD0 N7 AD0



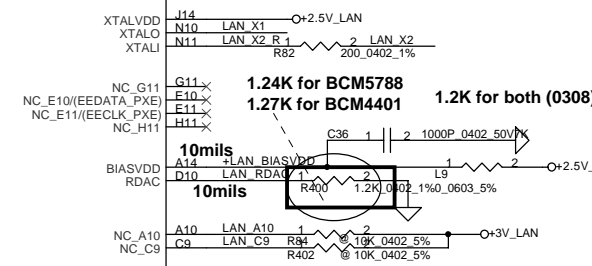
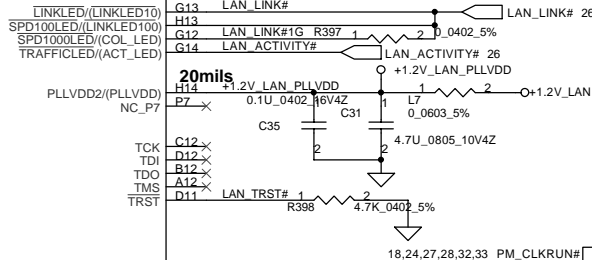
16,22,24,27,28 PCI_C/BE#3 C4 CBE3
16,22,24,27,28 PCI_C/BE#2 F3 CBE2
16,22,24,27,28 PCI_C/BE#1 I3 CBE1
16,22,24,27,28 PCI_C/BE#0 M4 CBE0

100_0402_5%
PCI_AD17 R96 1 2 LAN_IDSEL A4 IDSEL
16,22,24,27,28 PCI_FRAME# F1 FRAME
16,22,24,27,28 PCI_IRDY# E2 IRDY
16,22,24,27,28 PCI_TRDY# G3 TRDY
16,22,24,27,28 PCI_DEVSEL# H3 DEVSEL
16,22,24,27,28 PCI_STOP# J1 STOP
16,22,24,27,28 PCI_PERR# J2 PERR
16,22,24,27,28 PCI_SERR# A2 SERR
16,22,24,27,28 PCI_PAR J1 PAR
16,22,24,27,28 CLK_PCI_LAN A3 CLK_PCI_LAN

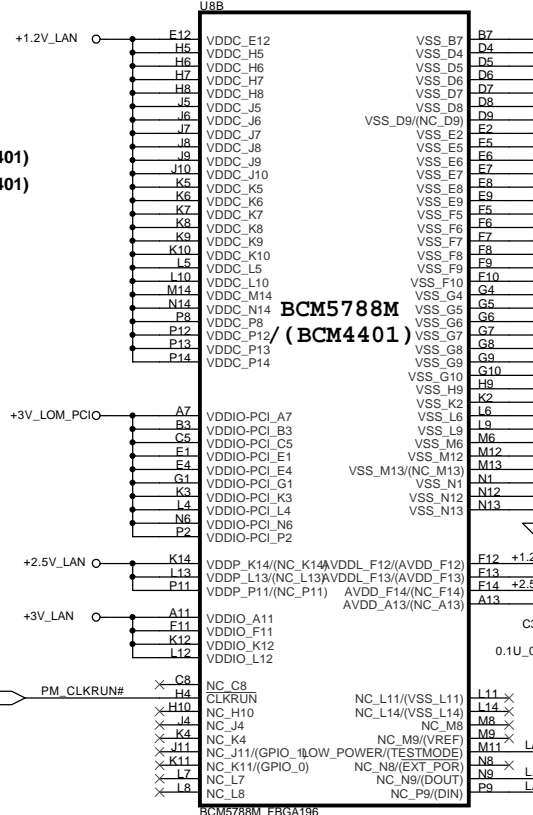
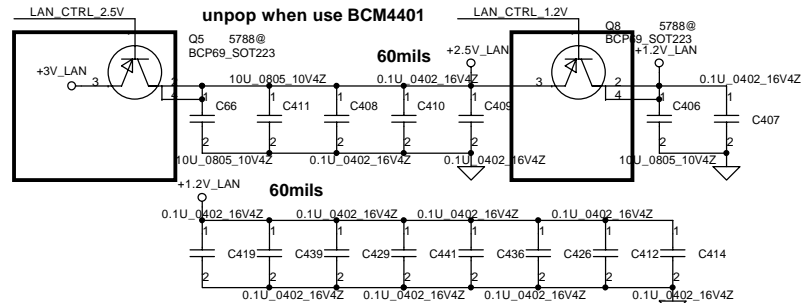
16 PCI_PIRQ# C2 INTA
16,22,24,27,28,32,33 PCI_RST# H2 PCI_RST
16 PCI_GNT#3 J3 GNT
16 PCI_REQ#3 C3 REQ



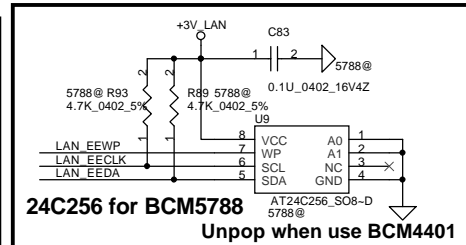
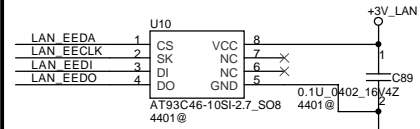
BCM5788M
/(BCM4401)



BCM5788M_FBGAT96

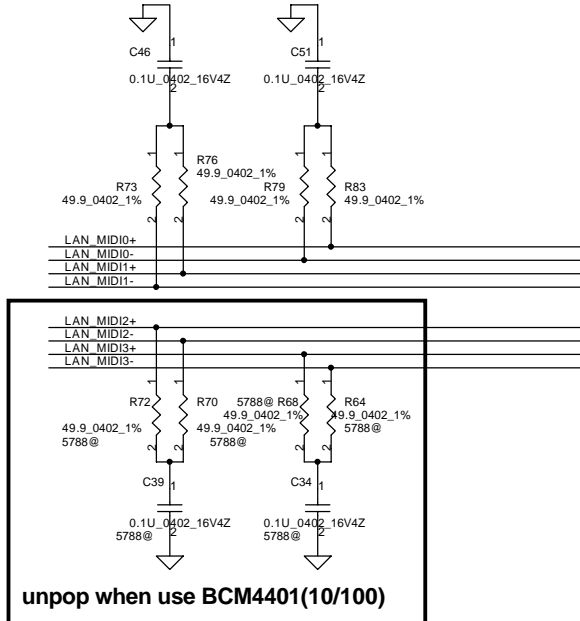
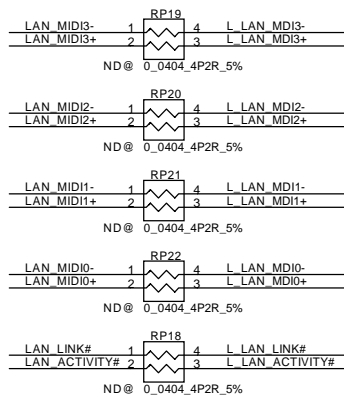


AT93C46 for BCM4401



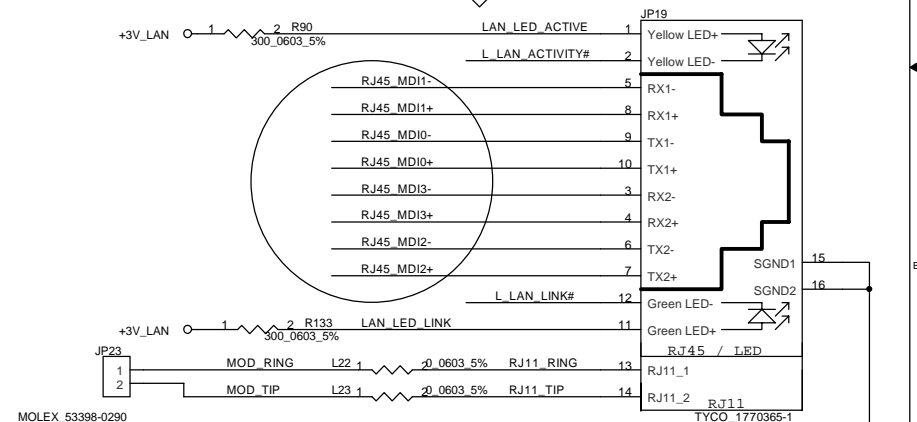
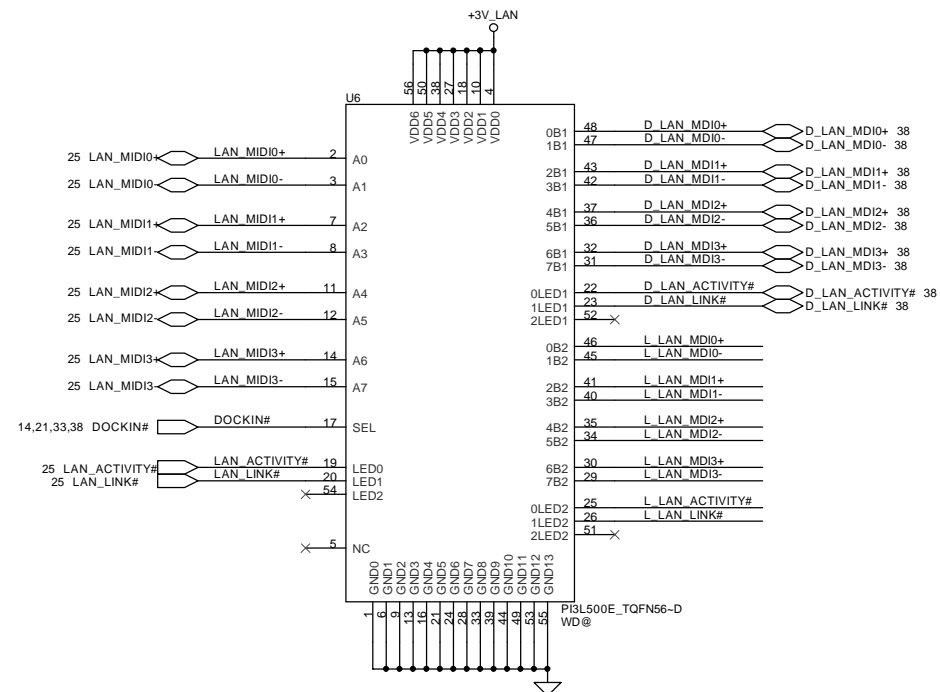
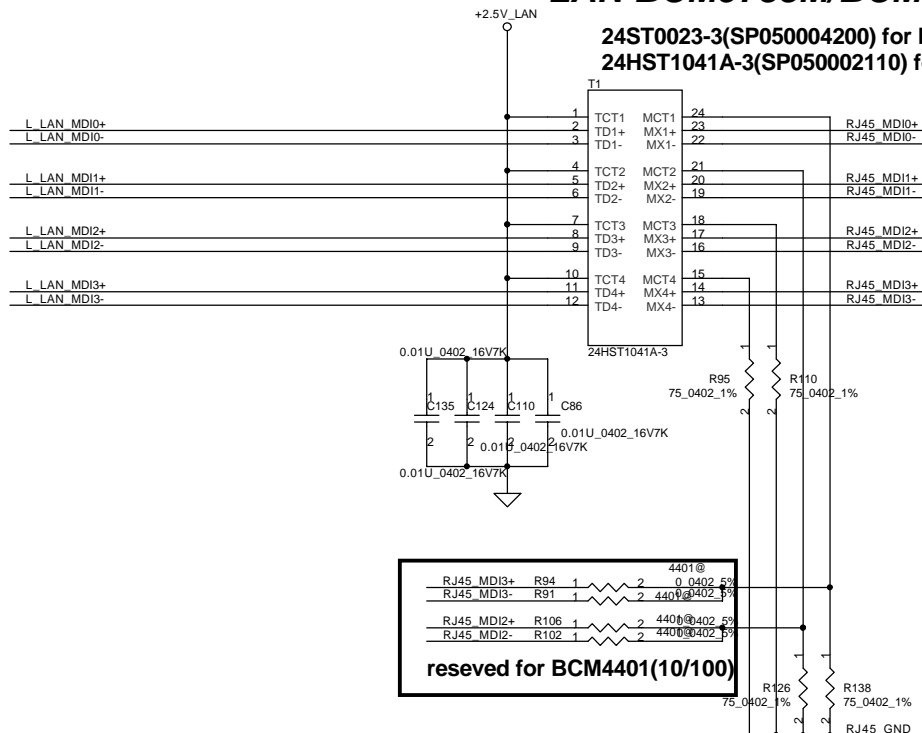
	5788	4401
L18		Pop
L19	Pop	
Q17	Pop	
Q18	Pop	
R318	1.24K	1.27K
U18		Pop
C424		Pop
U19	Pop	
R319	Pop	
R320	Pop	
C423	Pop	

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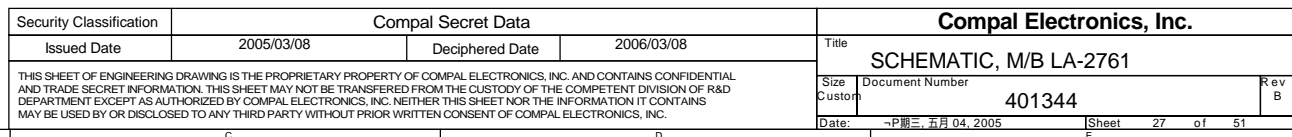


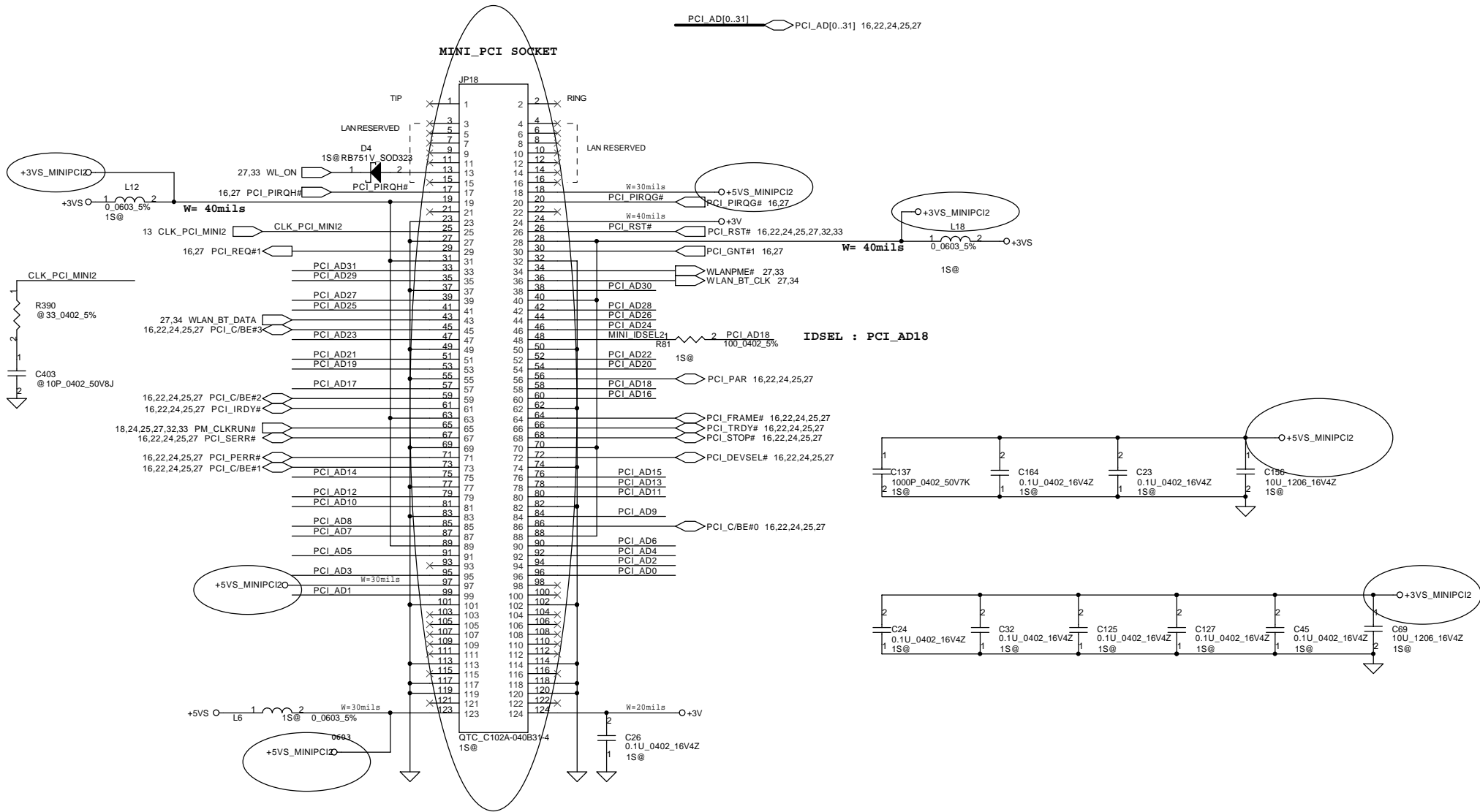
LAN BCM5788M/BCM4401KFB

24ST0023-3(SP050004200) for BCM4401(10/100)
24HST1041A-3(SP050002110) for BCM5788M(GbE)

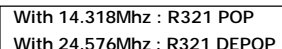
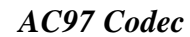
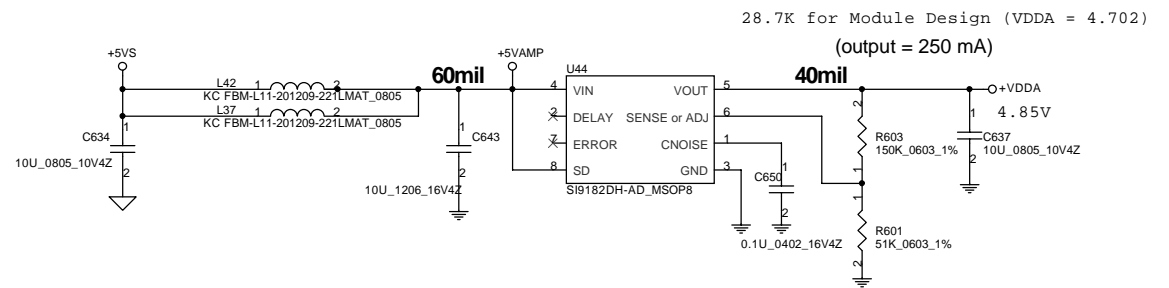


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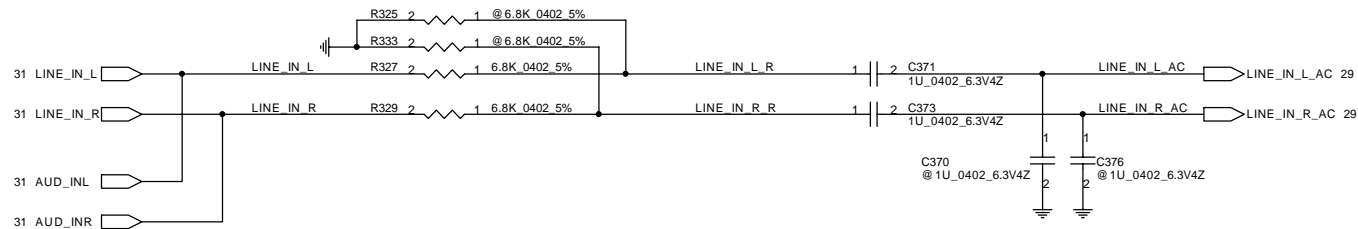




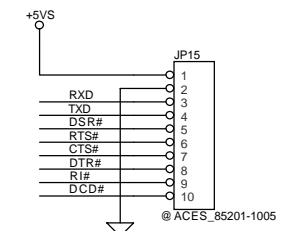
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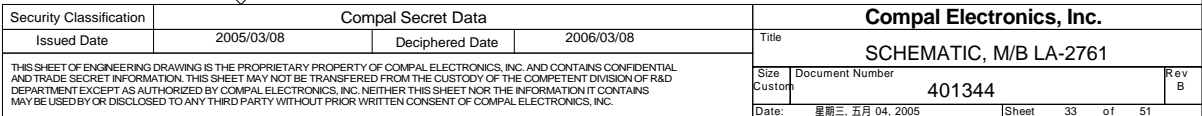
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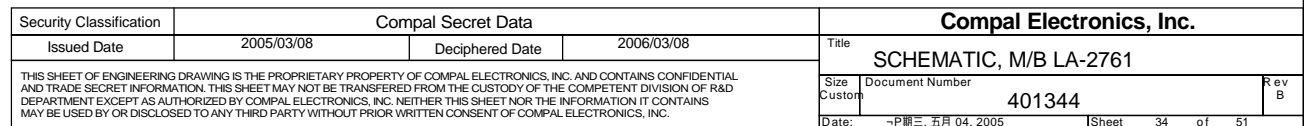
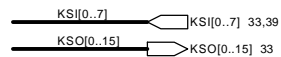


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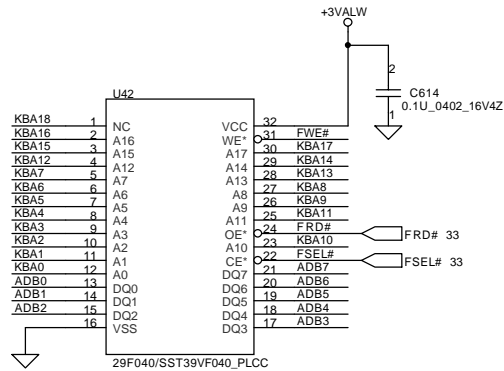


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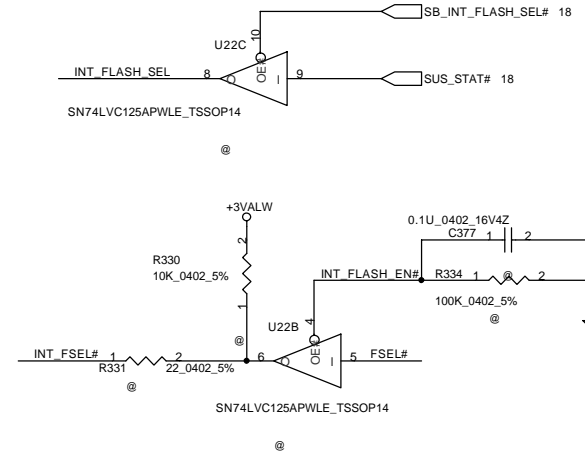
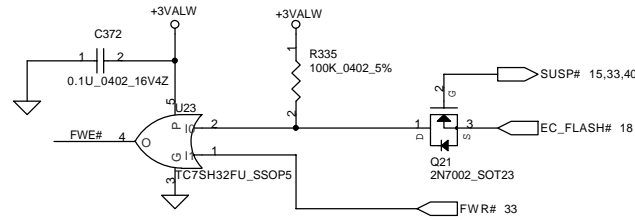




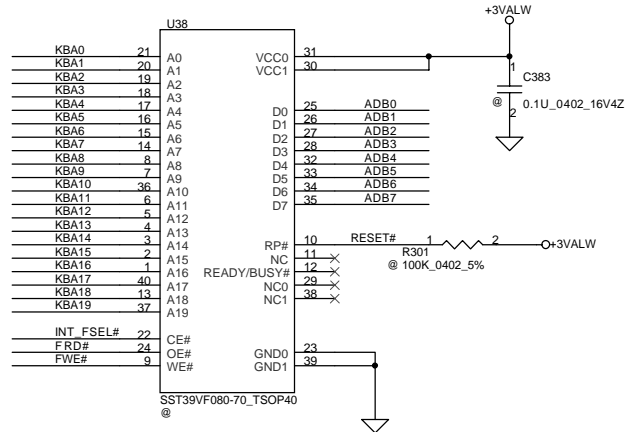
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33 ADB[0..7] ADB[0..7]



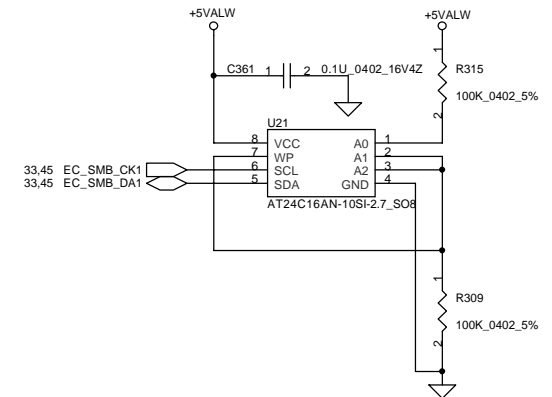
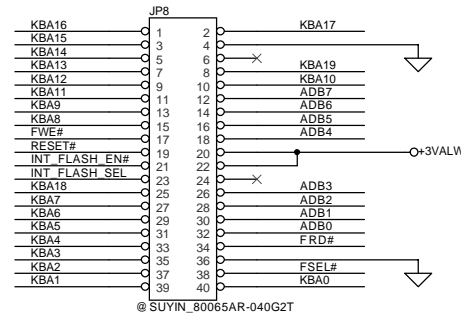
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1MB Flash ROM

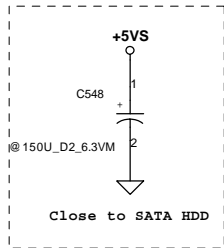
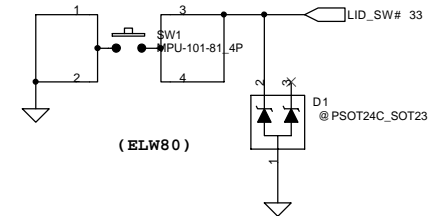
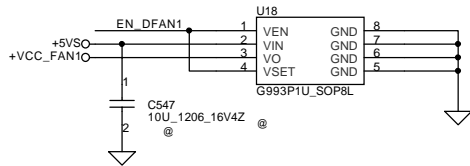
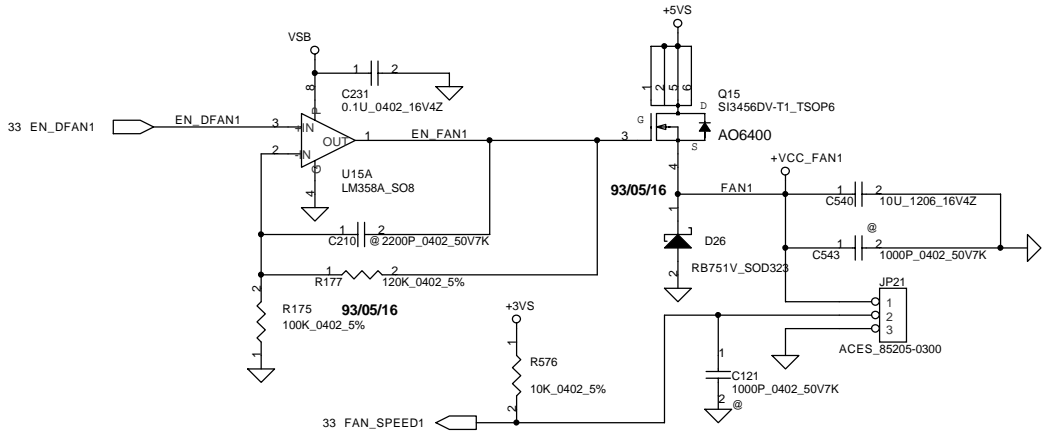


1MB ROM Socket

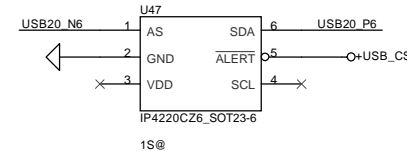
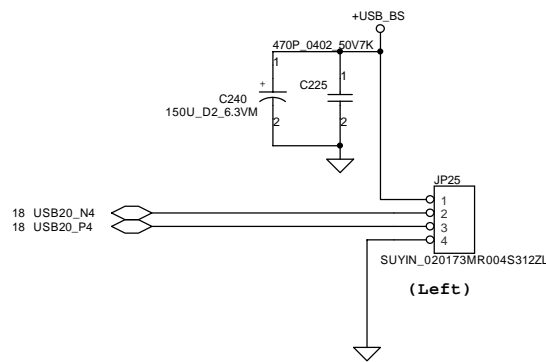
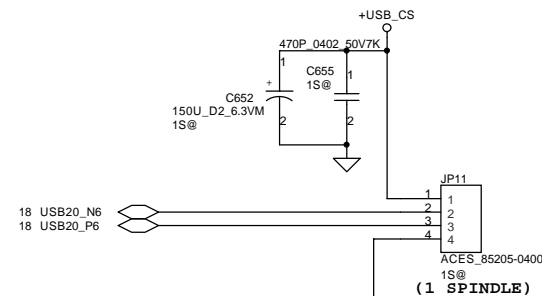
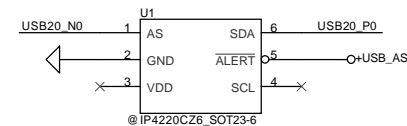
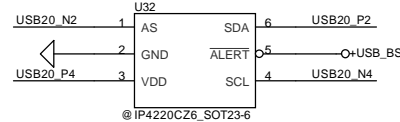
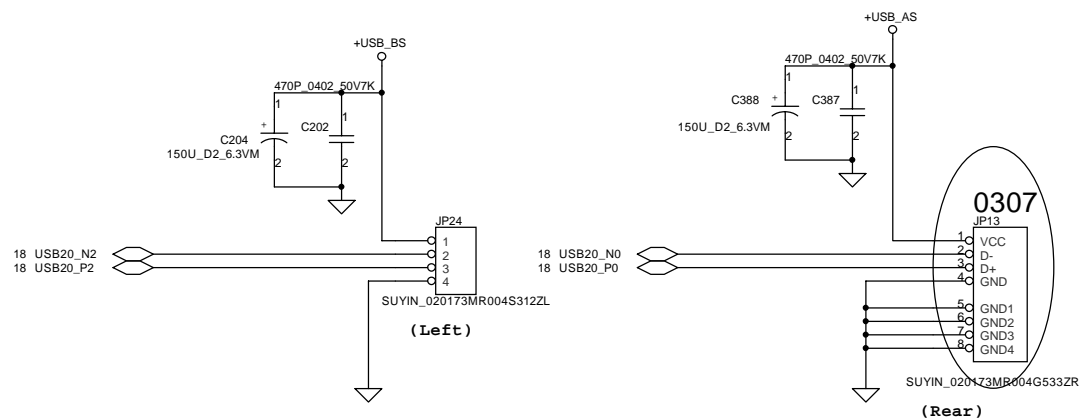
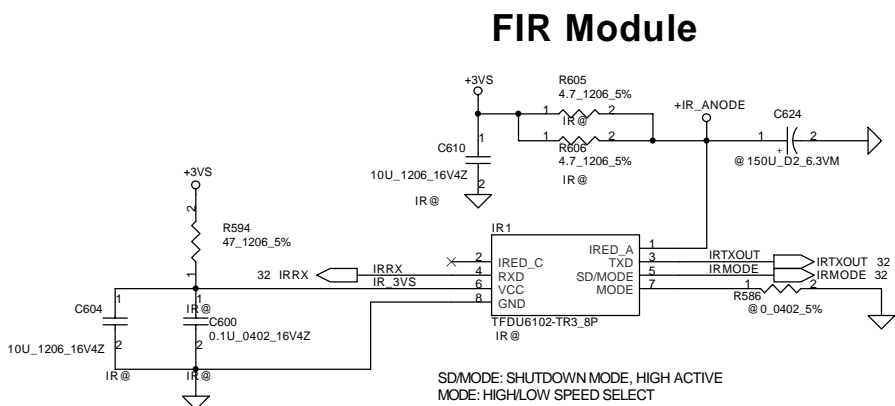
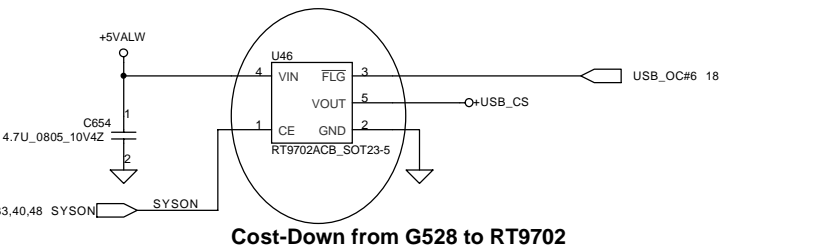
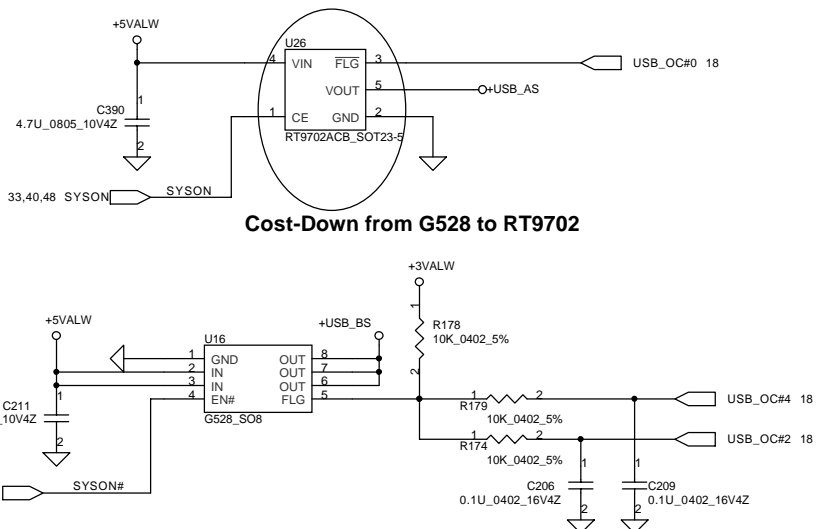


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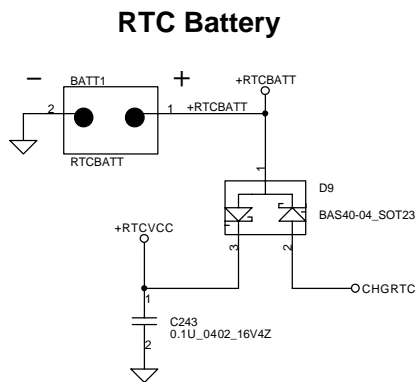
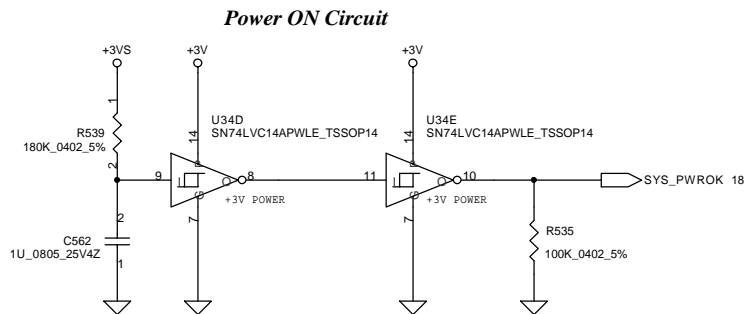
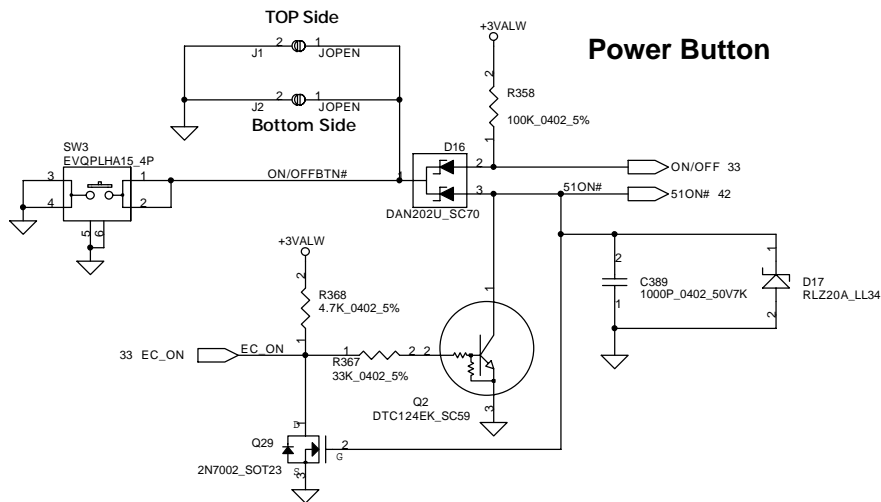
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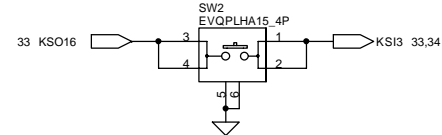
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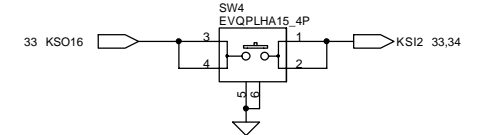
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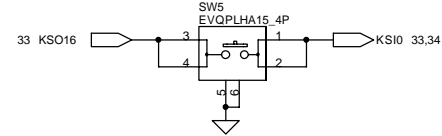
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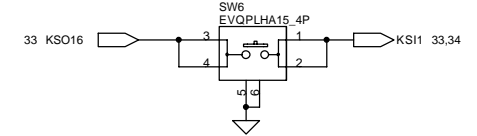
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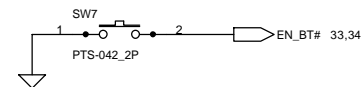
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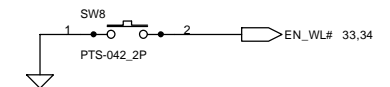
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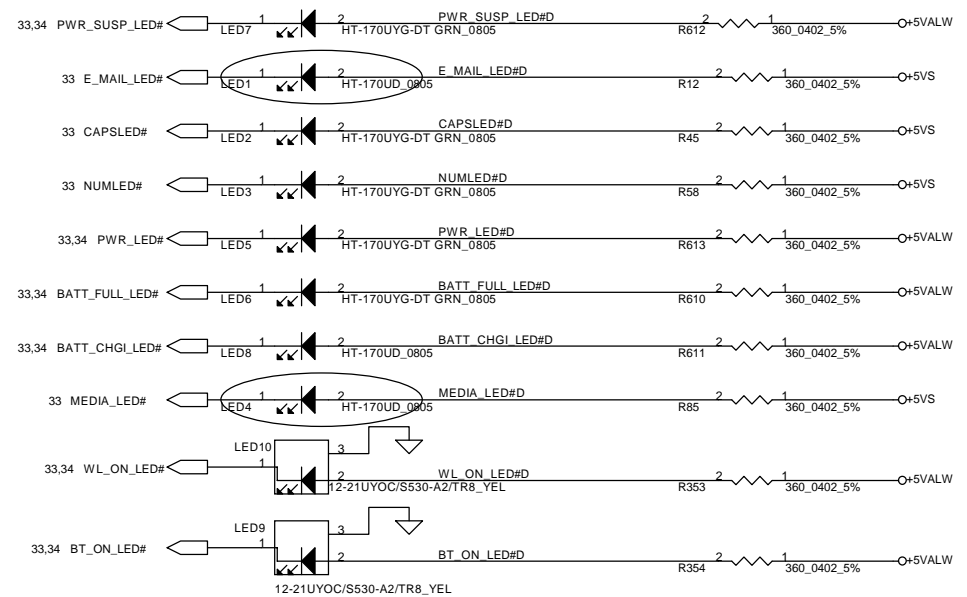
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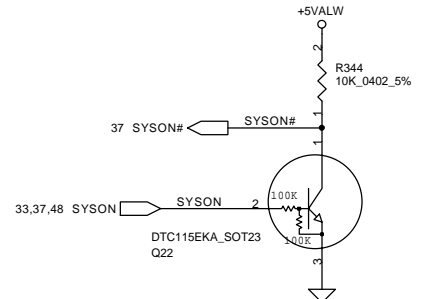
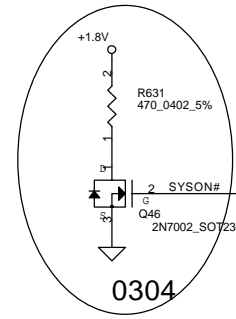
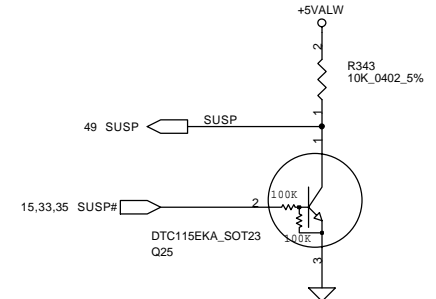
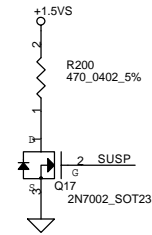
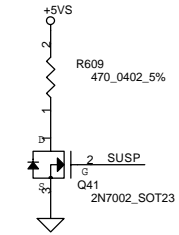
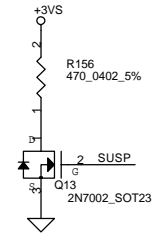
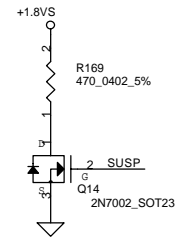
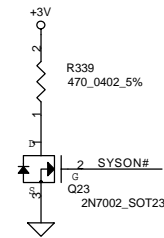
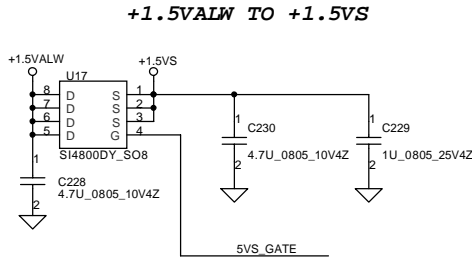
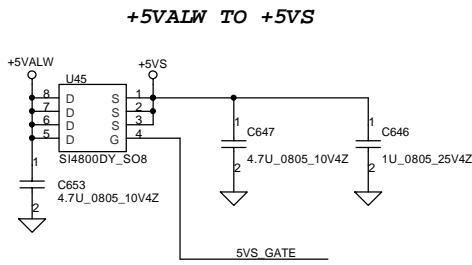
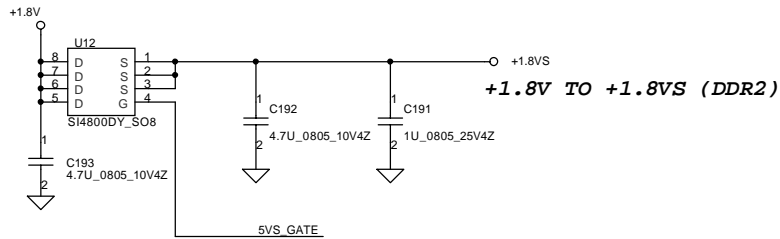
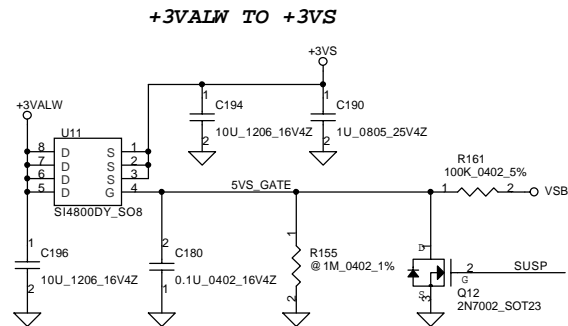
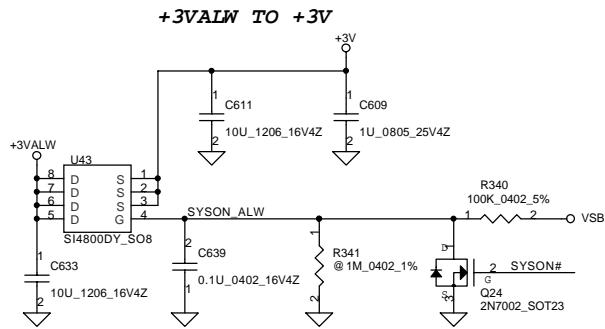
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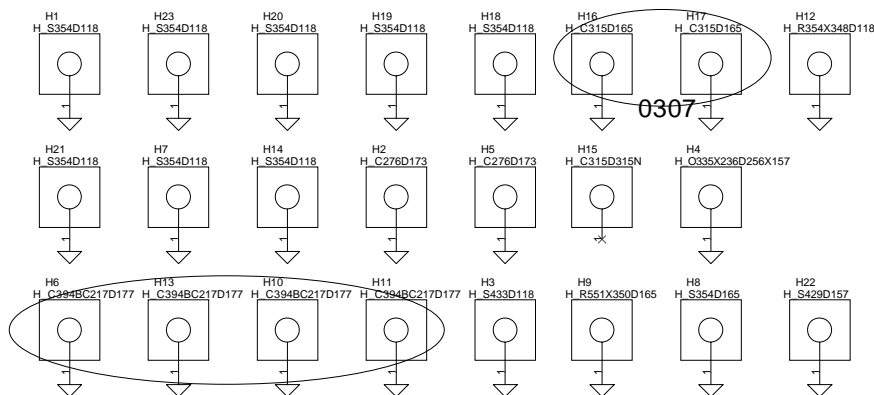
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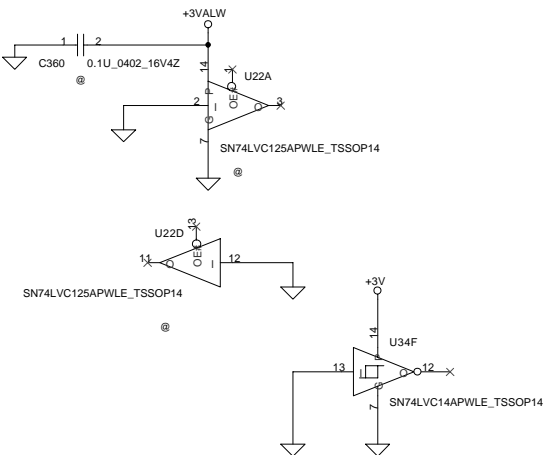
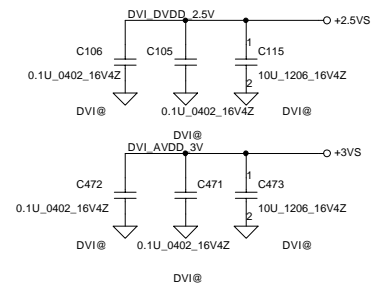
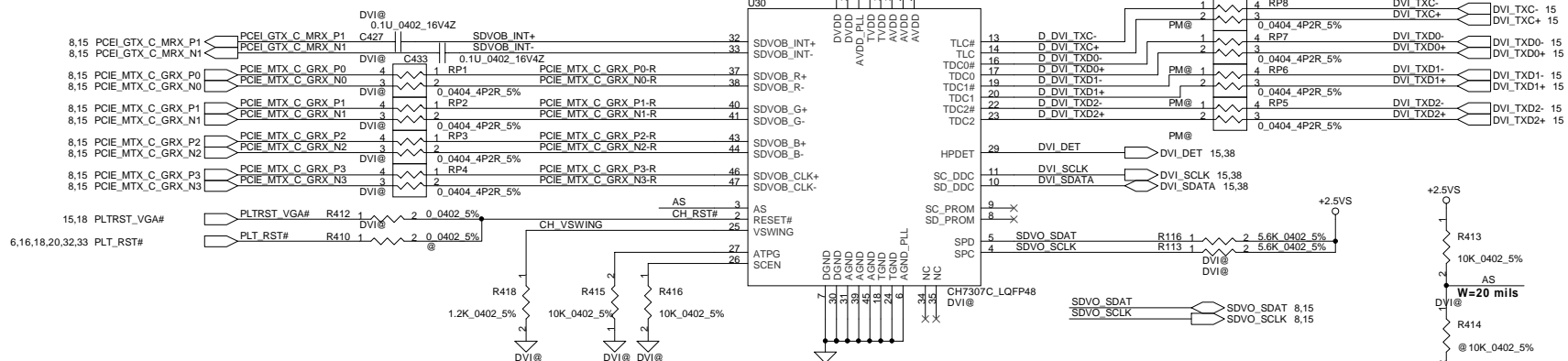
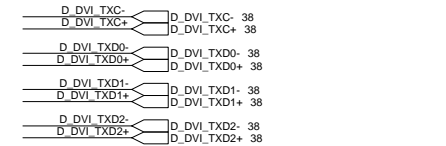
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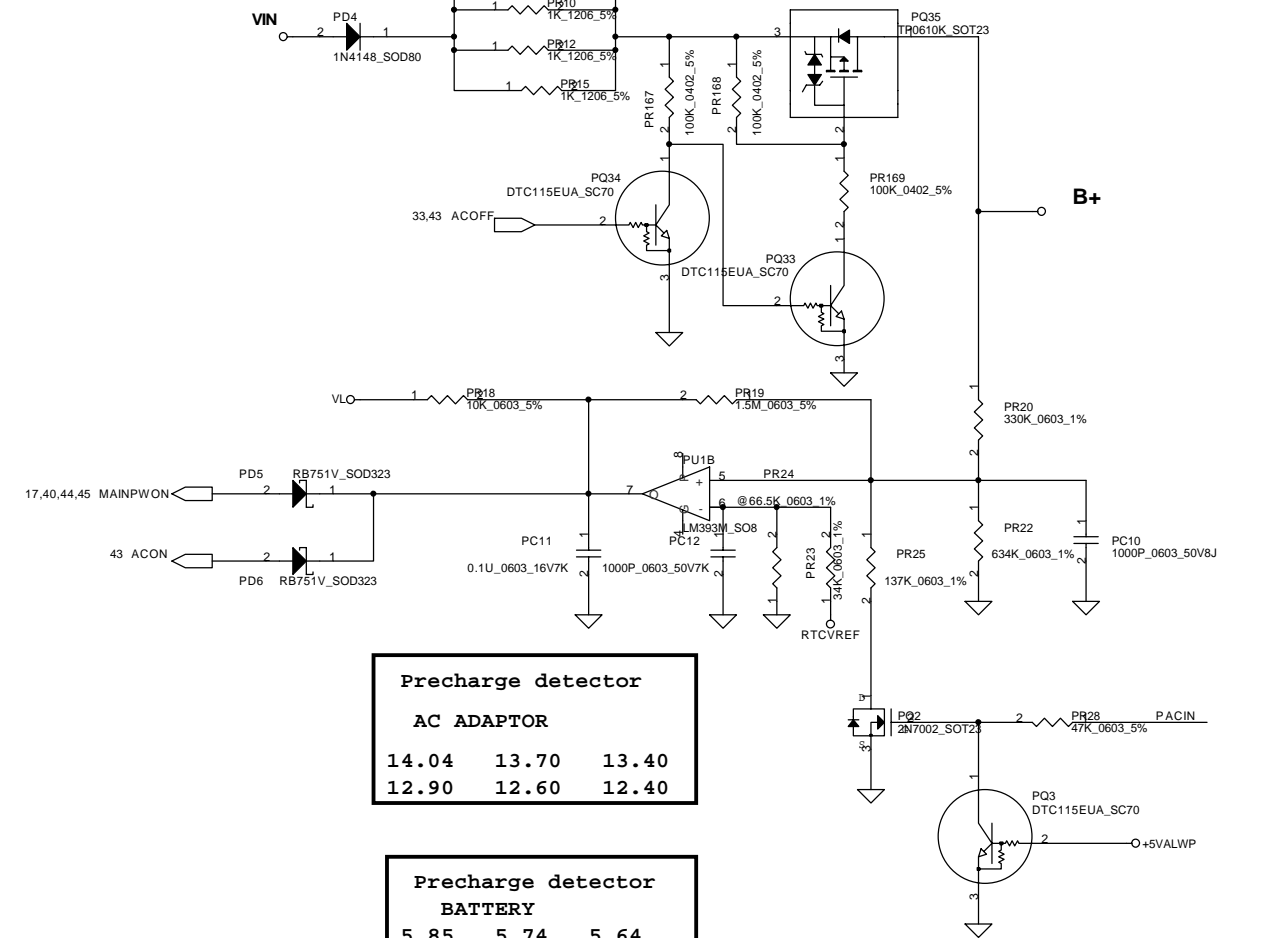
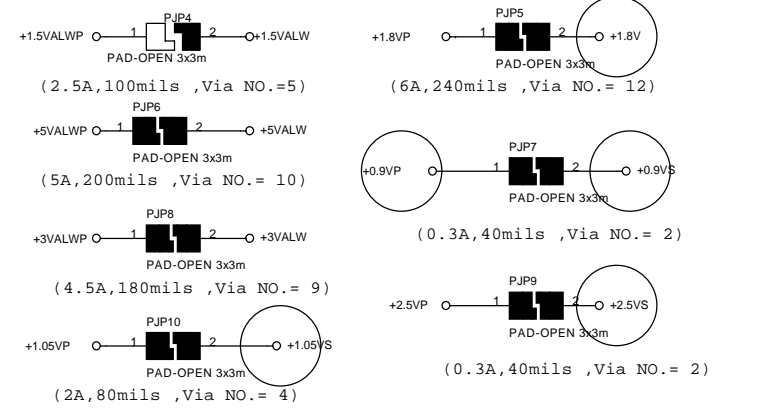
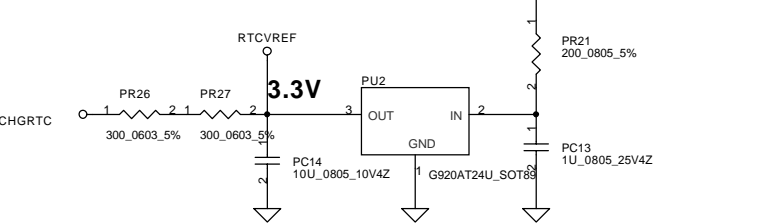
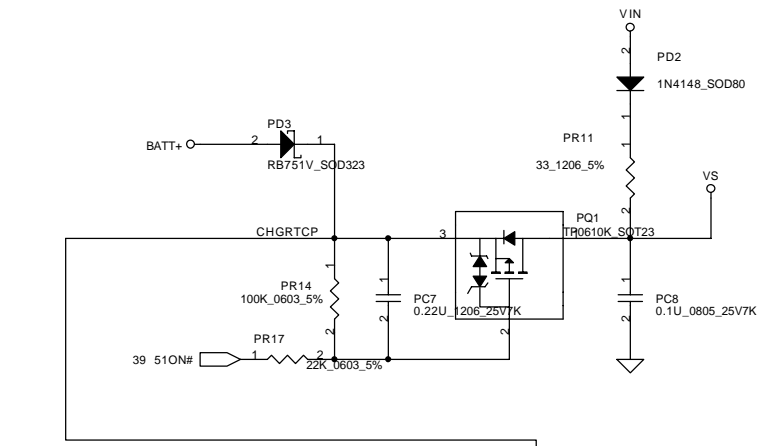
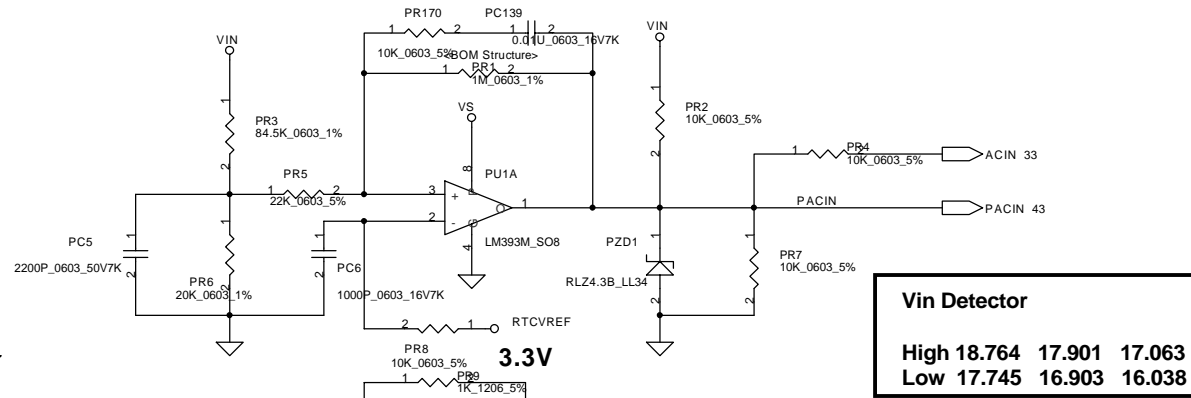
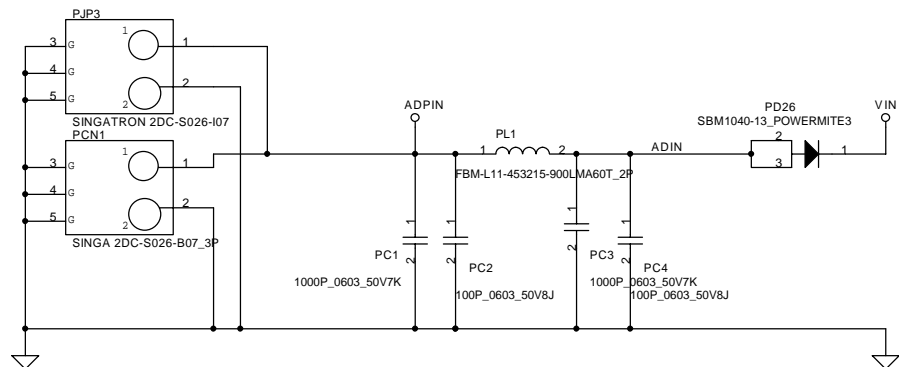
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Chip_Name	Label_Name
EXP_TXP0/SDVOB_RED	PCIE_MTX_C_GRX_P0
EXP_TXN0/SDVOB_RED#	PCIE_MTX_C_GRX_N0
EXP_TXP1/SDVOB_GREEN	PCIE_MTX_C_GRX_P1
EXP_TXN1/SDVOB_GREEN#	PCIE_MTX_C_GRX_N1
EXP_TXP2/SDVOB_BLUE	PCIE_MTX_C_GRX_P2
EXP_TXN2/SDVOB_BLUE#	PCIE_MTX_C_GRX_N2
EXP_TXP3/SDVOB_CLKP	PCIE_MTX_C_GRX_P3
EXP_TXN3/SDVOB_CLKN	PCIE_MTX_C_GRX_N3
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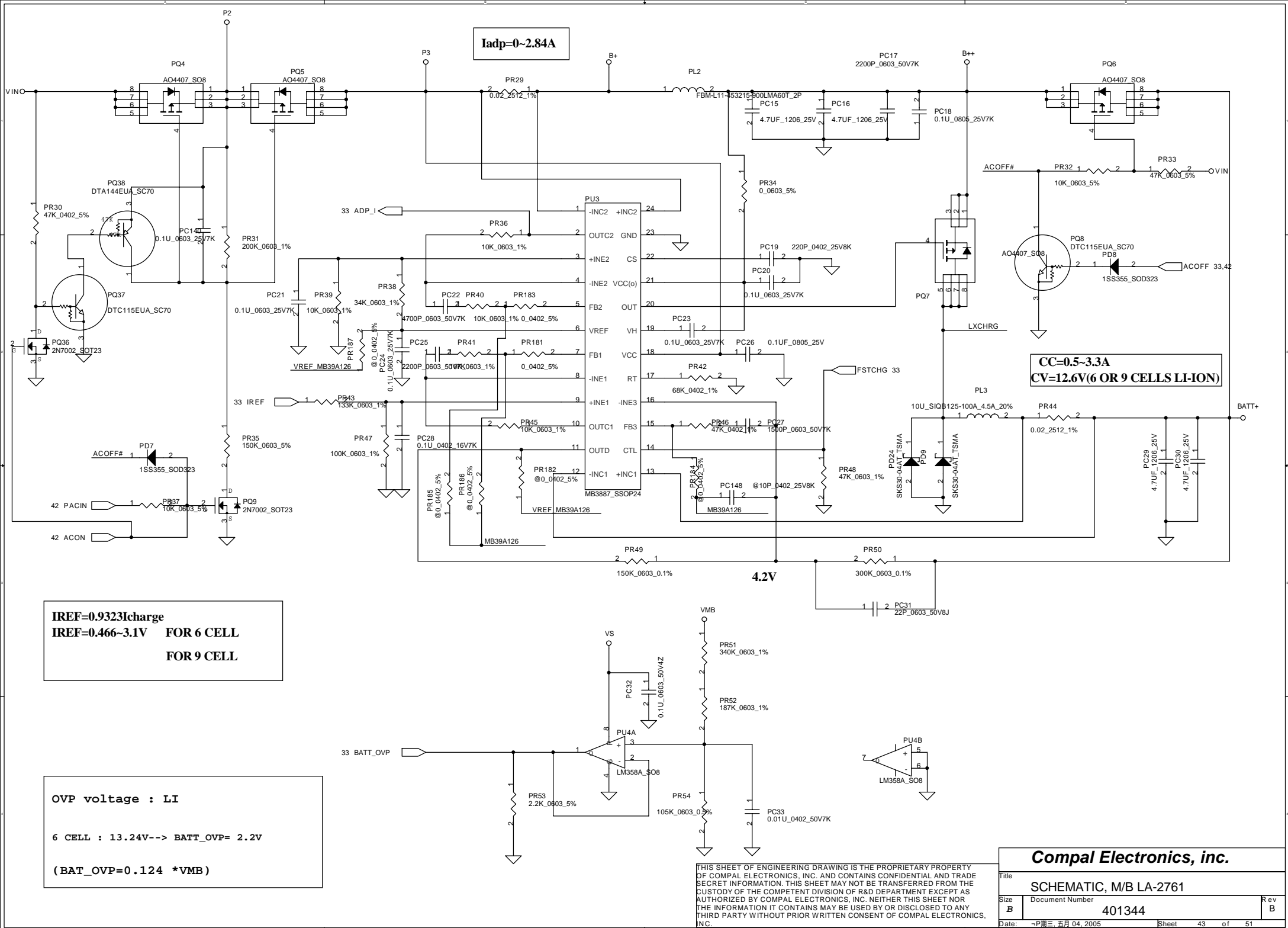
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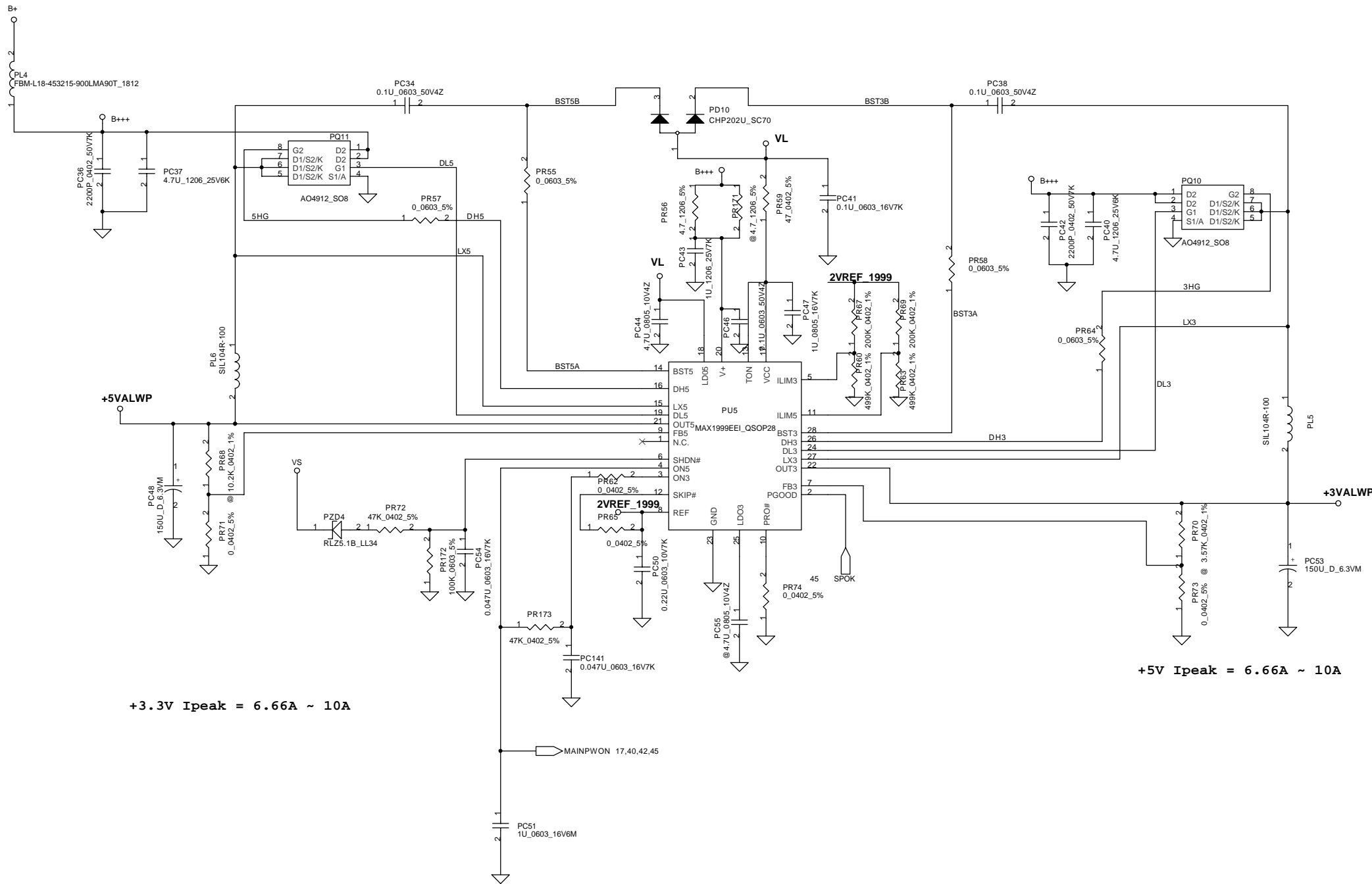


Precharge detector		
AC ADAPTOR		
14.04	13.70	13.40
12.90	12.60	12.40

Precharge detector		
BATTERY		
5.85	5.74	5.64
4.74	4.65	4.60

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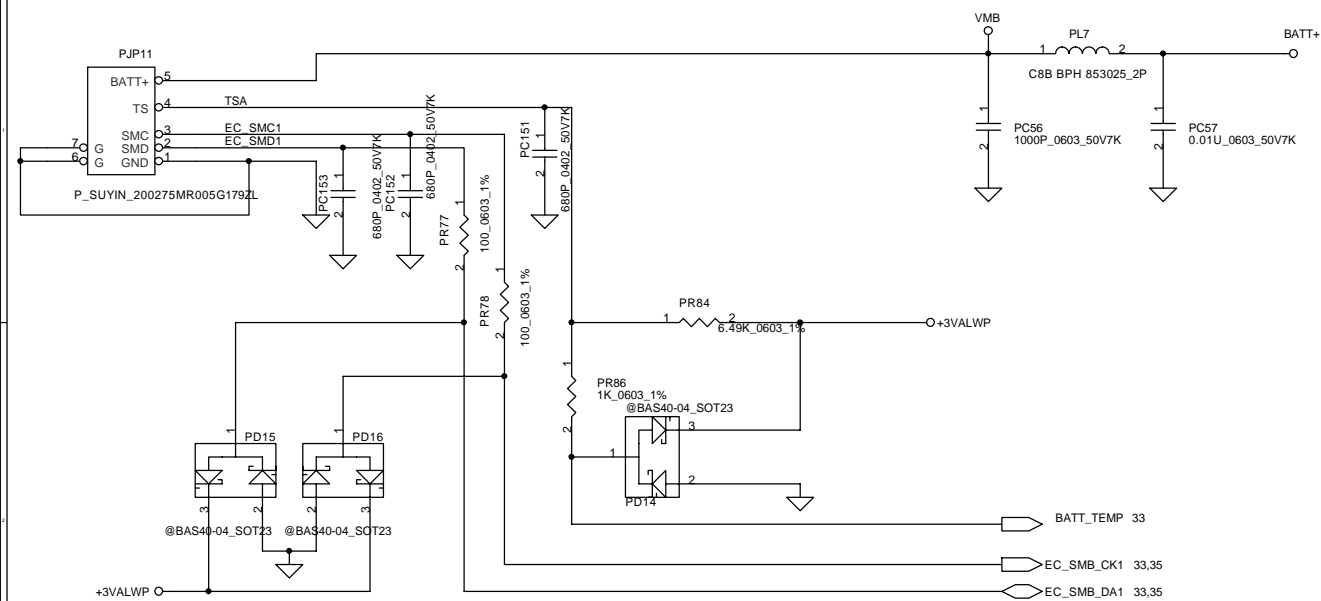


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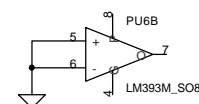
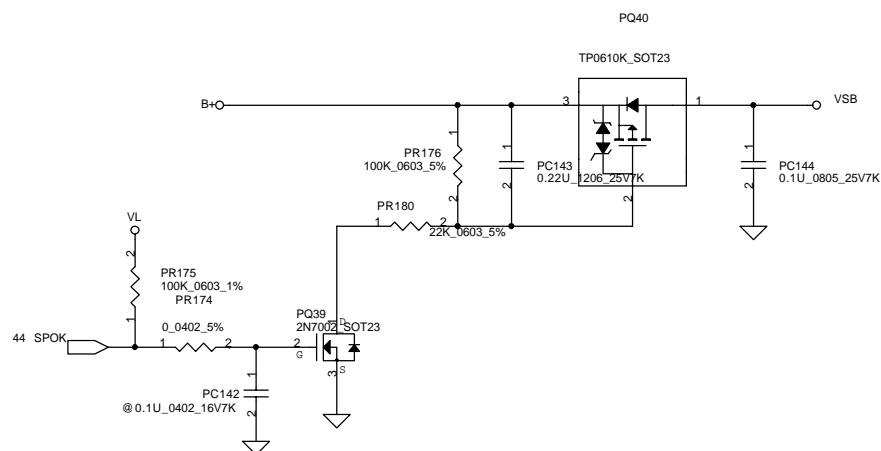
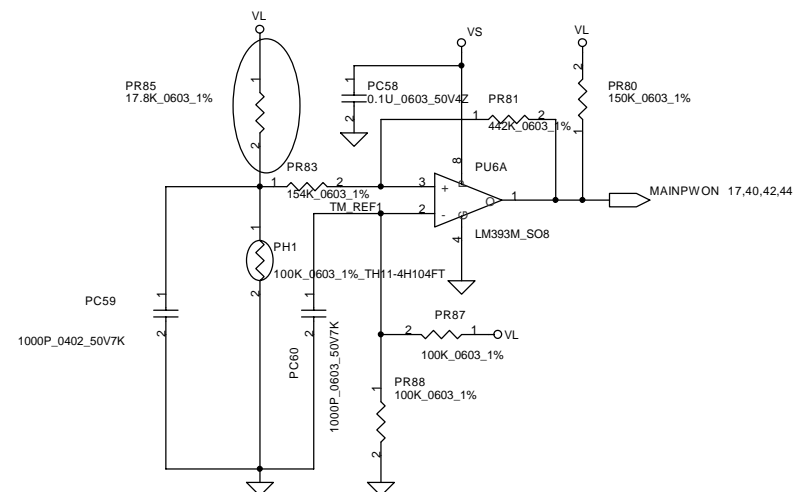
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PH1 under CPU bottom side :
 CPU thermal protection at 80 degree C
 Recovery at 44(45) degree C

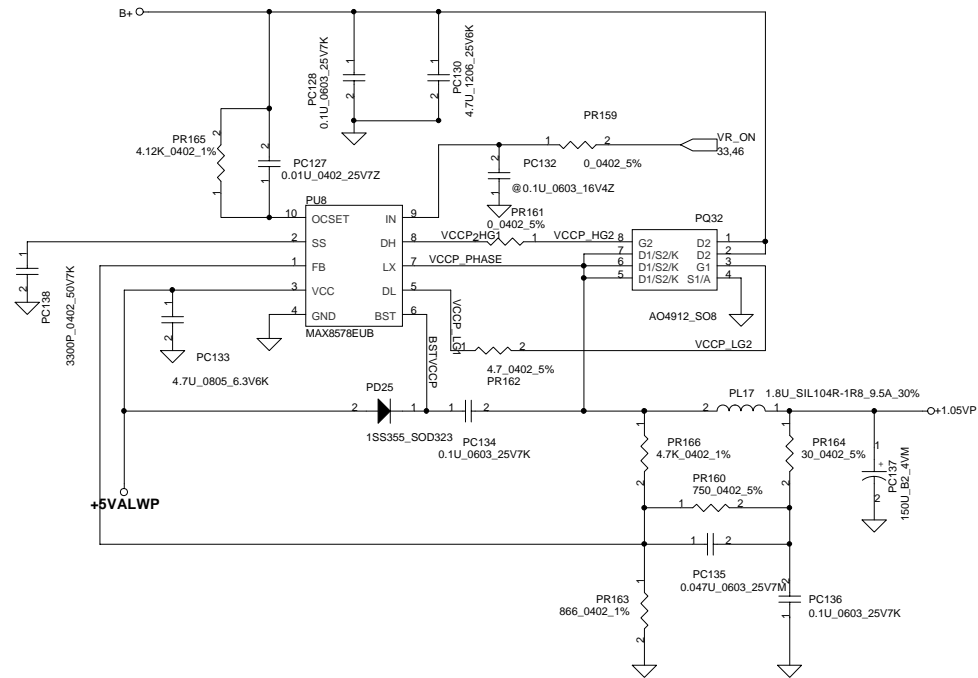


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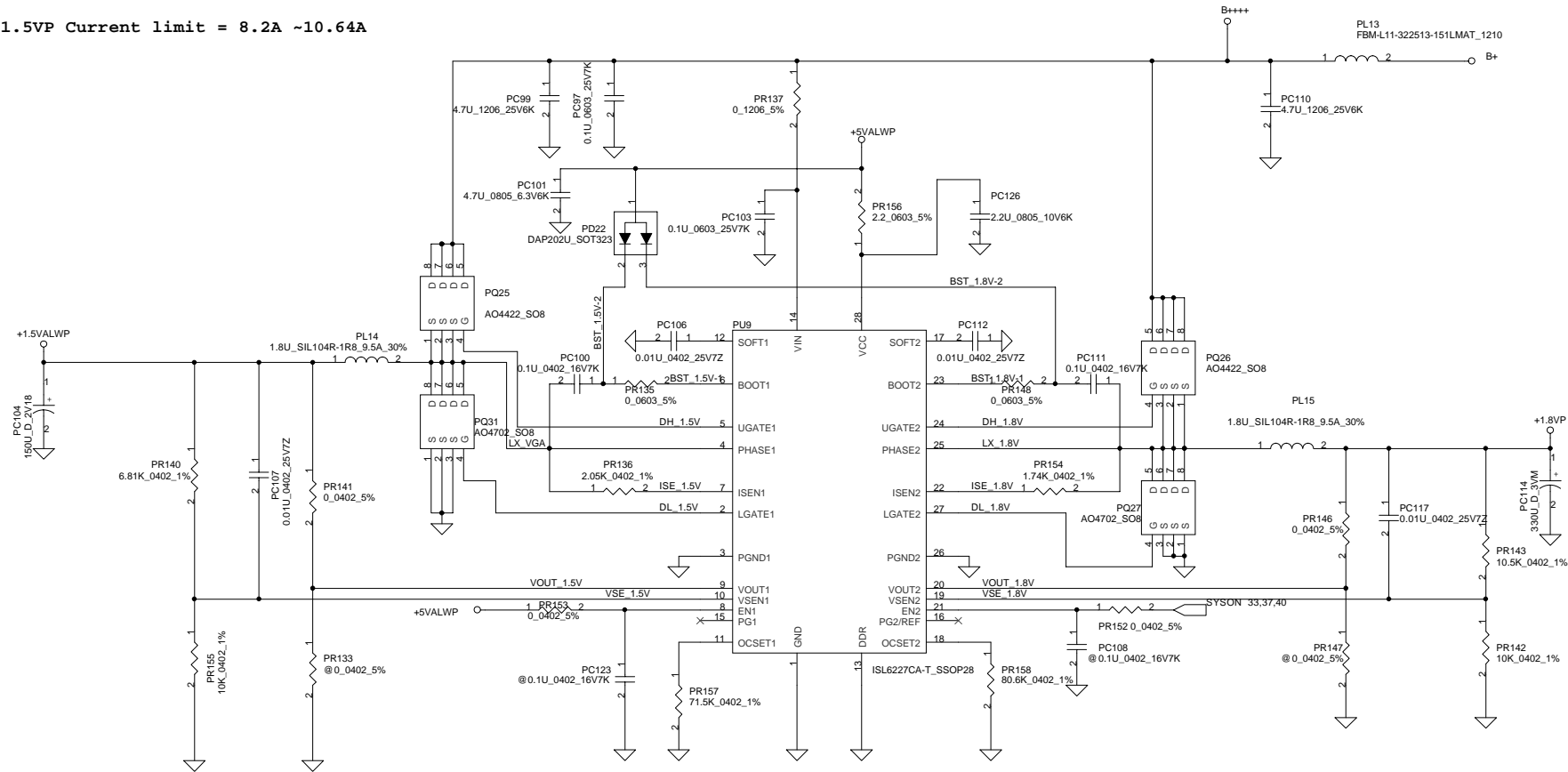


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+1.8VP O.C.P. =8.1A ~ 10.36A

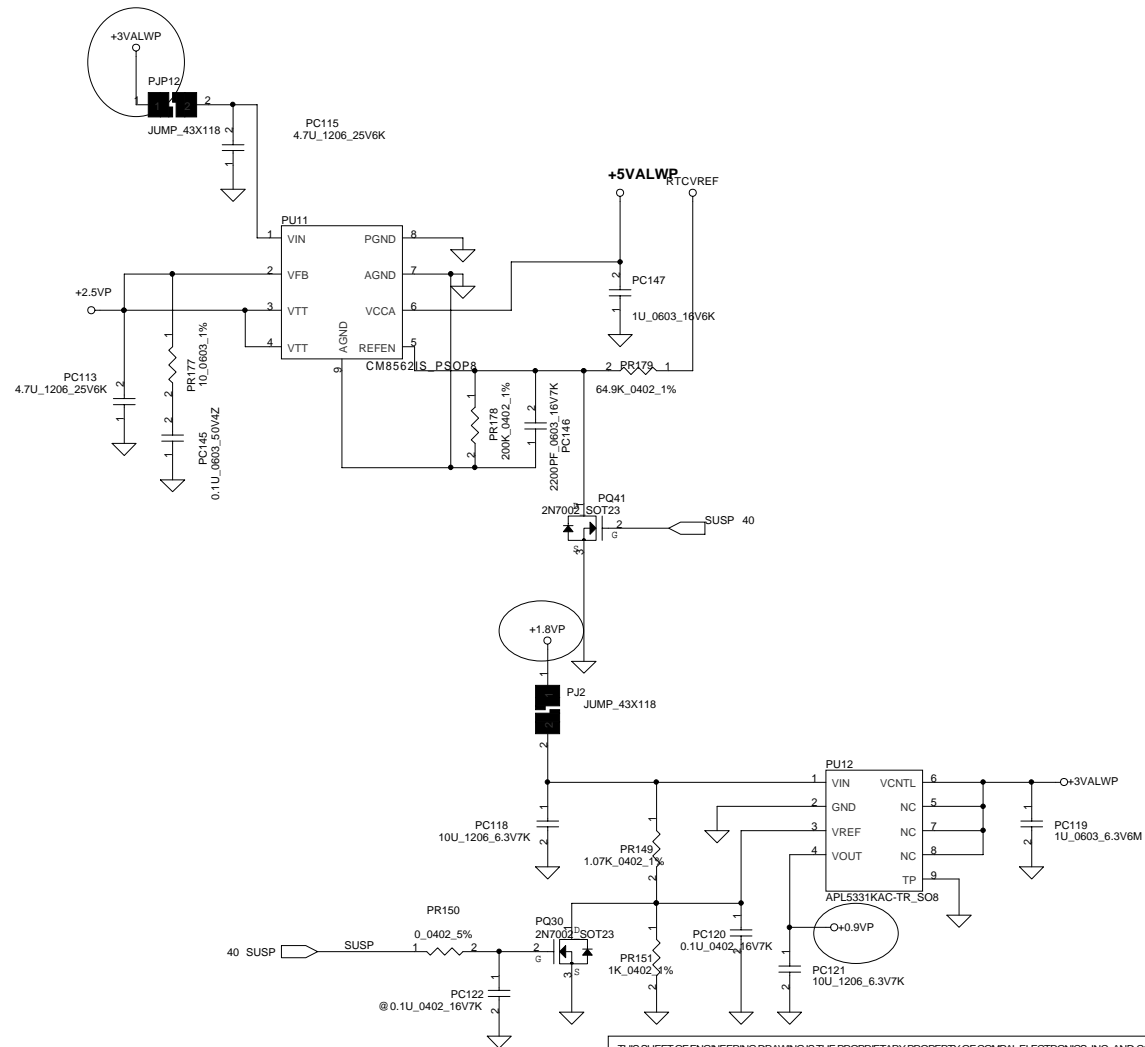
+1.5VP Current limit = 8.2A ~10.64A



915PM(DISCRETE)	PR143=11K 1.8VP=1.867V
915GM (UMA)	PR143=10.5K 1.8VP=1.8V

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Version change list (P.I.R. List)

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	VER	Phase
1	Delete R45,R47	EMI cost down	0.2	28			
2	Add R505,C627	Improve signal quality					
3							
4							
5							
6							
7							
8							
9							

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Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	VER	Phase
1		Modify PU8 IC		47	1. change PU8 from MAX8576 to MAX8578 2.delete PC132		DVT
2		Modify 1.05VP voltage level		47	1. change PR166 from 7.15k to 4.7k , PC135 from 6800P to 0.047u 0603		DVT
3		change precharge detect point		42	1. change PR20 from 412k to 330k		DVT
		Modify disable precharge respond time		42	1. change PR167/PR168/PR169 from 470k to 100k		DVT
		PD16 PIN2 AND PIN3 are wrong		45	1. modify PD16 schematic,and delete PD16(only reserve)		DVT
		for decrease BATT connector EMI		45	1. add PC151/PC152/PC153 :680PF		DVT
		for decrease CPU CORE switching ring effect and add EMI solution : snubber		46	1. change PR94/PR116 from 0 to 2.2 2. add PR189//PR188 4.7 1206 ,add PC150/PC149 680P		DVT
4							
5							
6							
7							
8							
9							